

NSH079N15D



N-Channel Enhanced Shielded Gate Trench Power MOSFET

Voltage: 150 Volts

Current: 115 Ampers

Package: TO-263

Features

- NH'S Advanced SGT Technology
- Low Reverse Transfer Capacitances
- Low Gate Charge For Low Switching Loss
- Excellent $Q_g \cdot R_{ds(on)}$ Product(FOM)

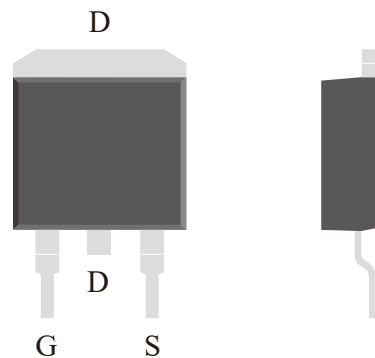
Typical Applications

- Synchronous Rectification
- High-Frequency Circuits
- The Motor Drives
- Automotive Electronics

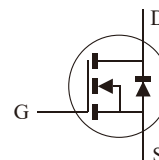
Product Summary

VDS Min.@T _j	150	V
I _D Min.@Ta	115.1	A
RDS(ON)(TYP)@10V	7.9	mΩ

Diagram:



Polarity:



***100% UIS TESTED**

***100% DVSD TESTED**

Absolute Maximum Ratings (Ta=25°C Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Ratings	Unit
Drain-Source Voltage		V _{DS}	150	V
Gate-Source Voltage		V _{GS}	±20	V
Continuous Drain Current (Note 1)	Ta= 25 °C	I _D	115	A
	Ta= 100 °C		92	
Drain Current-Pulse (Note 1)	T _j < 175 °C	I _{DM}	383	A
Maximum Power Dissipation Power Dissipation Derating Factor Above 25°C	Ta= 25 °C	P _D	283	W
	Ta= 100 °C		142	
Derating Factor		D _F	1.89	W/°C
Avalanche Current,Single Pulse (Note 1)	L= 0.5 mH	I _{AS}	36.0	A
Single Pulse Avalanche Energy (Note 1) Test Circuit & Waveform See Fig.16	L= 0.5 mH IAS= 36.0 A, RG= 10.0 Ω Starting T _j =25 °C, VG= 10.0 V	E _{AS}	324	mJ

Thermal Characteristics (Ta=25°C Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Typ.	Unit
Junction Temperature		T _J	-55 to 175	°C
Storage Temperature Range		T _{STD}	-55 to 175	°C
Thermal Resistance Junction To Ambient With Steady-State	Still Air Environment With Ta=25°C	R _{θJA}	60.0	°C/W
Thermal Resistance Junction-Case With Steady-State	Device Mounted On 1 in2 FR-4 Board With 2oz. Copper	R _{θJC}	0.53	

Notes: 1.Pulse Width Limited By Max. Junction Temperature. (See Fig. 13).

NSH079N15D

N-Channel Enhanced Shielded Gate Trench Power MOSFET
Electrical Characteristics (Ta=25°C Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Static Off Characteristics						
Drain-Source Breakdown Voltage	VGS=0V,ID=250uA	BV _{DSS}	150	--	--	V
Bvdss Temperature Coefficient	ID=250uA,Reference25℃	ΔBV _{DSS} /ΔT _J	--	0.14	--	V/℃
Drain-Source Leakage Current	VDS= 150 V,VGS=0V	I _{DSS}	--	--	1	uA
Gate-Body Leakage Current	VGS= ±20 V,VDS=0V	I _{GSS}	--	--	±100	nA
Forward Transconductance	ID= 20.0 A,VDS= 5 V	g _{fs}	--	70.0	--	S
Static On Characteristics						
Gate Threshold Voltage	VGS= VDS ID=250uA	V _{GS(TH)}	2.0	3.0	4.0	V
Drain-Source On Resistance	ID= 20.0 A,VGS= 10.0 V	R _{DS(ON)}	--	7.9	8.8	mΩ
	ID= 20.0 A,VGS= 4.5 V		--	9.1	11.8	
Dynamic Characteristics						
Input Capacitance	VDS= 50 V	C _{iss}	--	4300.0	--	pF
Output Capacitance	VGS= 0 V	C _{oss}	--	330.0	--	pF
Reverse Transfer Capacitance	F= 1 MHZ	C _{rss}	--	12.0	--	pF
Switching Paramters (Test Circuit & Waveform See Fig.14)						
Turn-On Delay Time	VDS= 75 V	t _{d(on)}	--	18.0	--	ns
Turn-On Rise Time	VGS= 10.0 V	t _r	--	10.0	--	ns
Turn-Off Delay Time	RG= 10.0 Ω	t _{d(off)}	--	30.0	--	ns
Turn-Off Rise Time		t _f	--	12.0	--	ns
Gate Charge Paramters (Test Circuit & Waveform See Fig.15)						
Total Gate Charge	VDS= 75 V	Q _g	--	50.0	--	nC
Gate-Source Charge	VGS= 10.0 V	Q _{gs}	--	20.0	--	nC
Gate-Drain Charge	ID= 20.0 A	Q _{gd}	--	5.0	--	nC
Drain-Source Diode Characteristics And Maximum Ratings (Test Circuit & Waveform See Fig.17)						
Max. Diode Forward Cuurent		I _s	--	--	115	A
Max. Pulsed Forward Cuurent		I _{SM}	--	--	461	A
Diode Forward Voltage	ID= 20.0 A,VGS=0V	V _{SD}	--	0.85	1.3	V
Reverse Recovery Time	ID= 20.0 A,di/dt= 100 A/us	t _{rr}	--	80.0	--	ns
Reverse Recovery Charge	VGS= 10.0 V,VDD= 75 V	Q _{rr}	--	160.0	--	nC

NSH079N15D



N-Channel Enhanced Shielded Gate Trench Power MOSFET

Typical Characteristics Curves

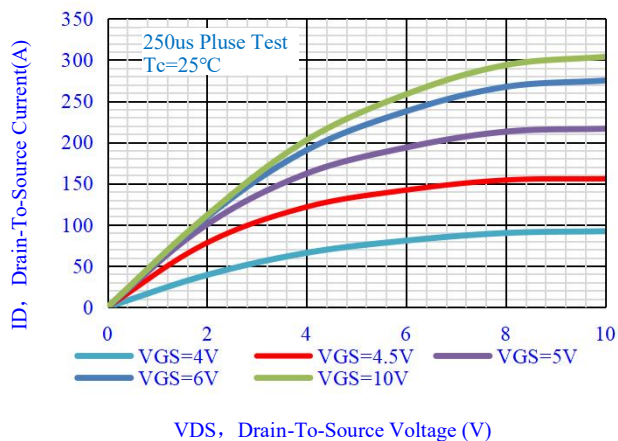


Fig.1-Output Characteristics

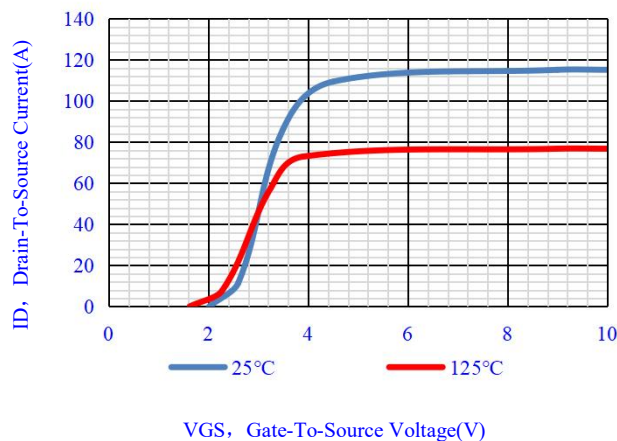


Fig.2- Transfer Characteristics

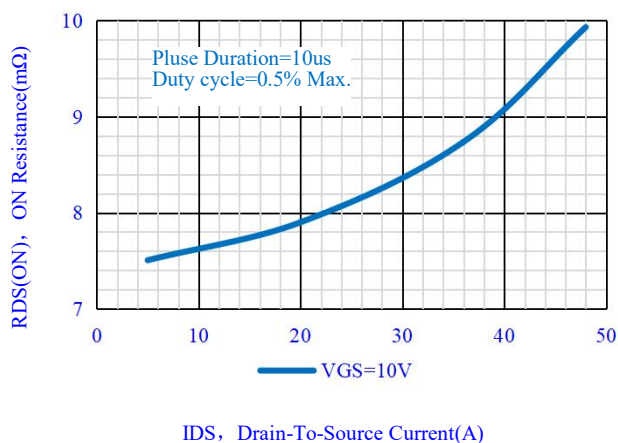


Fig.3- On Resistance vs. Drain Current

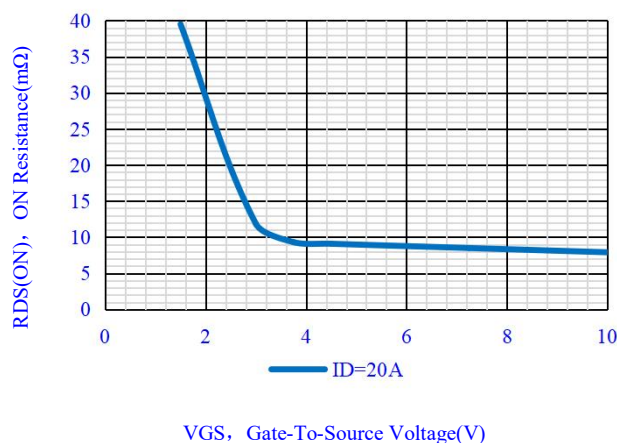


Fig.4- On Resistance vs. Gate Source Voltage

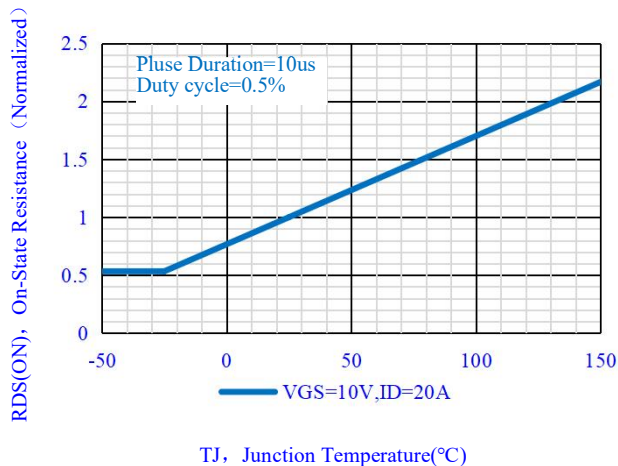


Fig.5- On Resistance vs. Junction Temperature

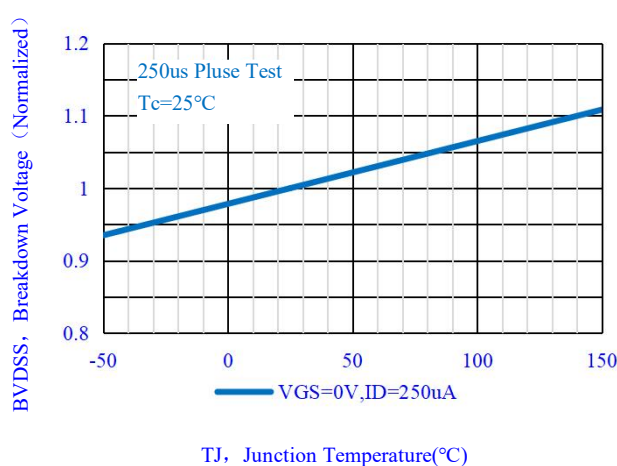


Fig.6- Breakdown Voltage vs. Junction Temperature

NSH079N15D

N-Channel Enhanced Shielded Gate Trench Power MOSFET



Typical Characteristics Curves

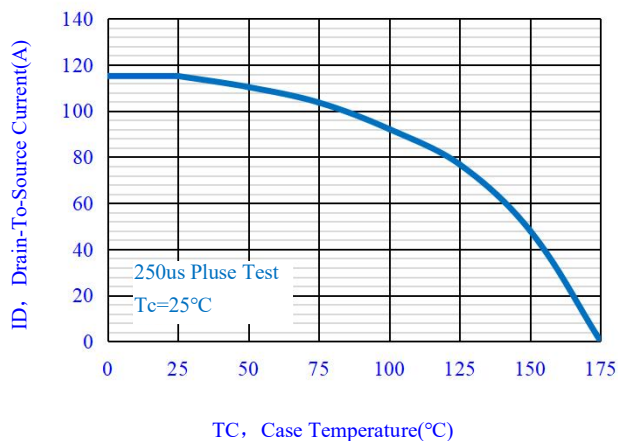


Fig.7-Maximum Continuous Drain Current vs. Case Temperature

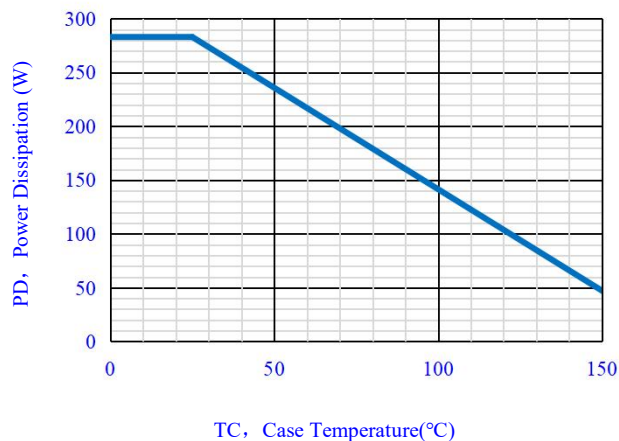


Fig.8-Maximum Power Dissipation vs. Case Temperature

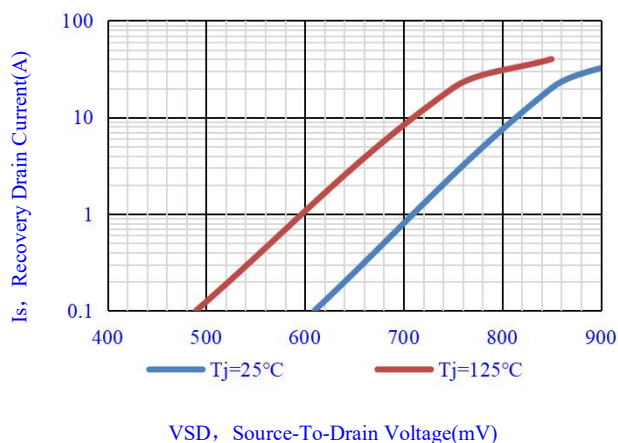


Fig.9- Source-To-Drain Diode Forward Voltage

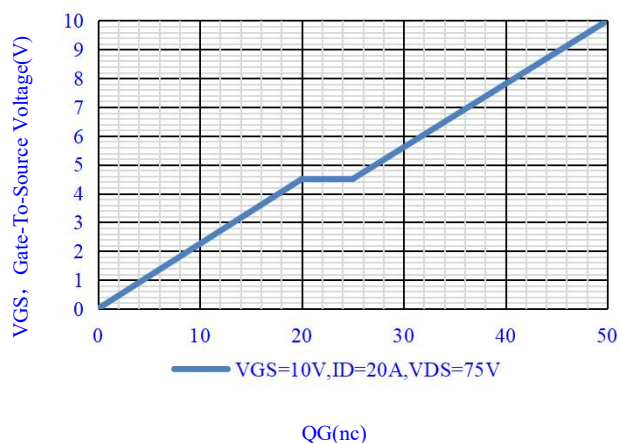


Fig.10-Gate Charge Waveform

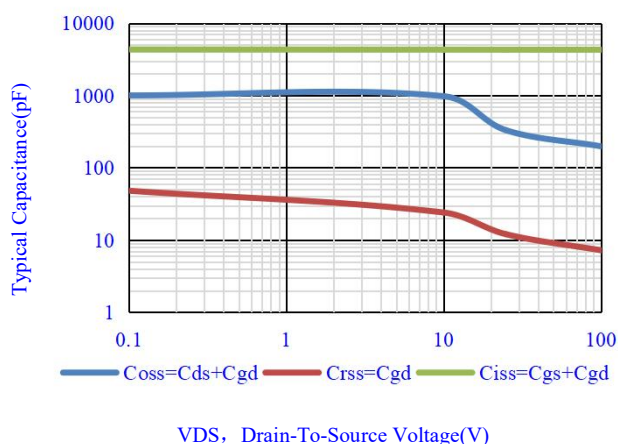


Fig.11-Typical Capacitance vs. Drain-To-Source Voltage

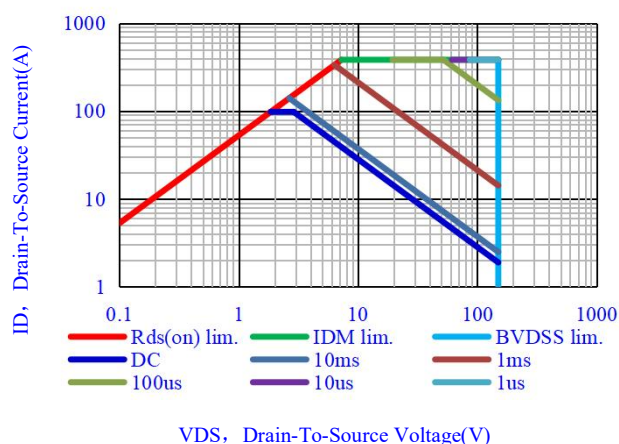


Fig.12-Maximum Safe Operating Area(SOA)

NSH079N15D

N-Channel Enhanced Shielded Gate Trench Power MOSFET



Typical Characteristics Curves

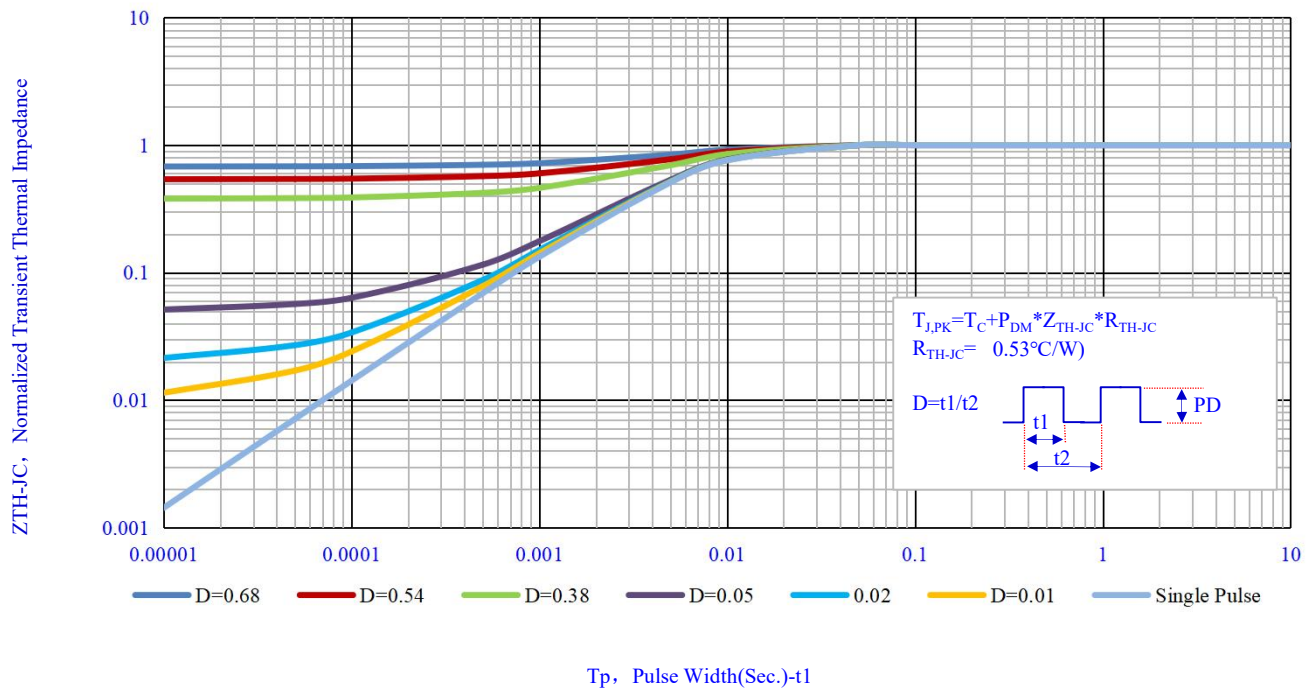


Fig.13- Normalized Maximum Transient Thermal Impedance vs.Pulse Width

Test Circuit & Waveform

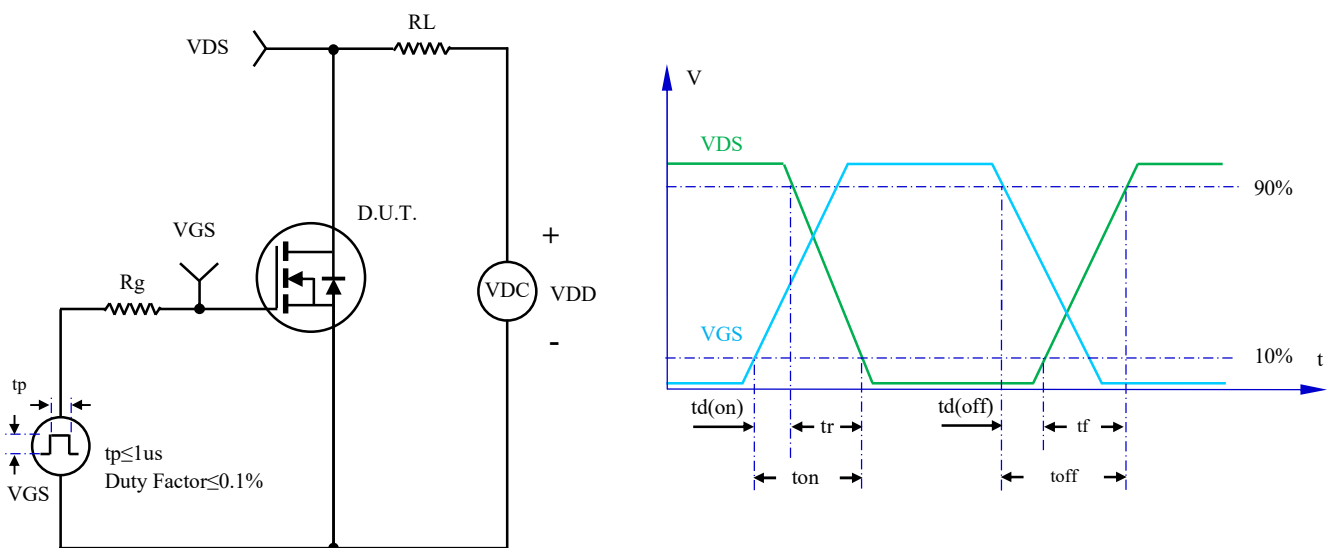


Fig.14- Resistive Switching Test Circuit & Waveform

NSH079N15D

N-Channel Enhanced Shielded Gate Trench Power MOSFET



Test Circuit & Waveform

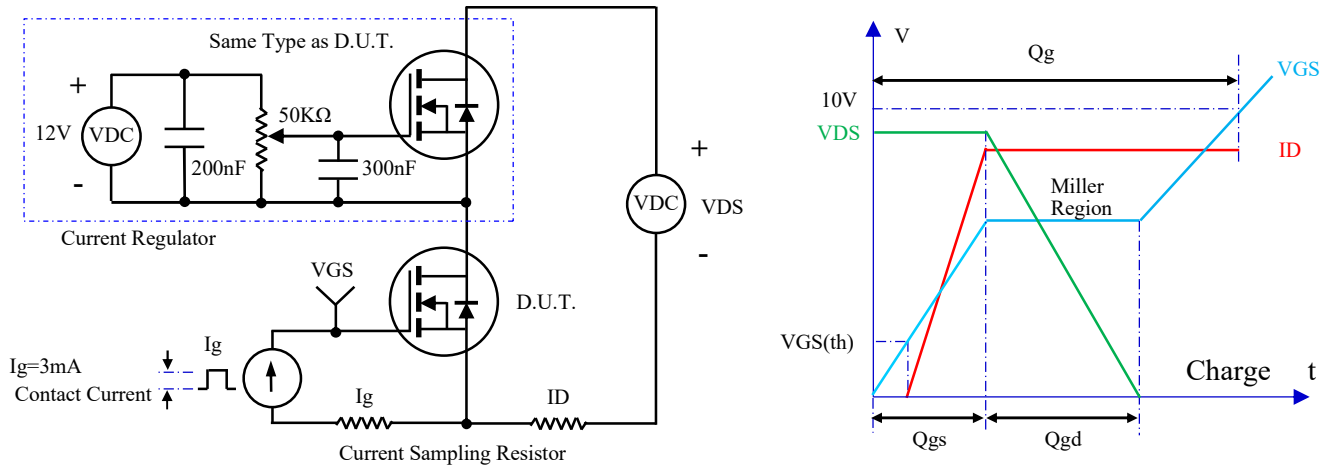


Fig.15- Gate Charge Test Circuit & Waveform

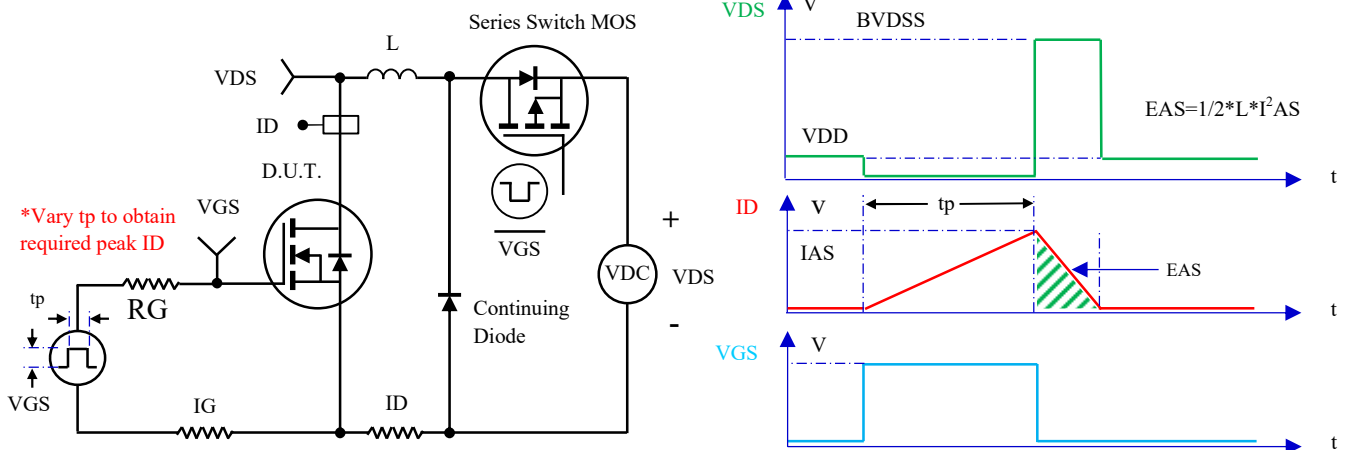


Fig.16- Unclamped Inductive Switching (UIS) Test Circuit & Waveform

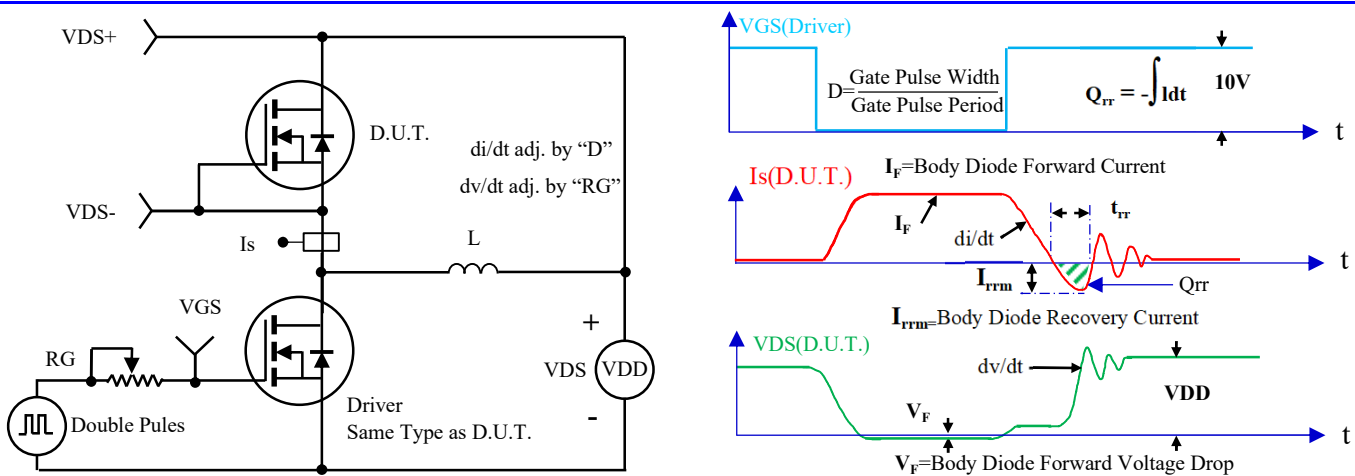


Fig.17- Diode Recovery Test Circuit & Waveform

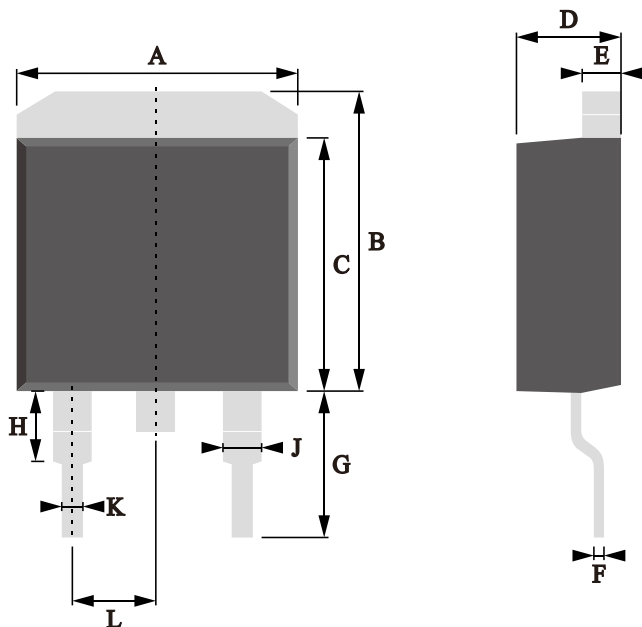
NSH079N15D

N-Channel Enhanced Shielded Gate Trench Power MOSFET



OUTLINE DRAWINGS

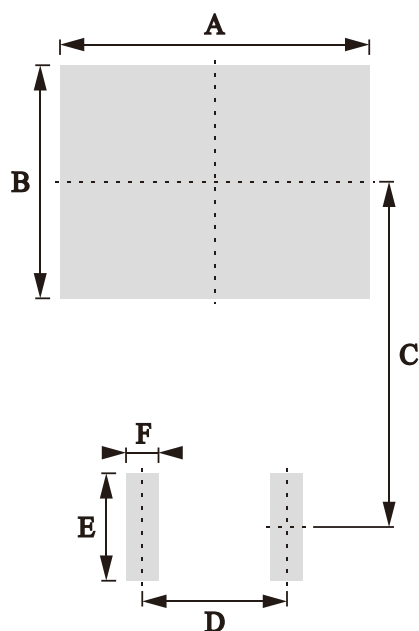
TO-263



OUTLINE DIMENSIONS						
Dim.	Milimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	9.30	-	10.50	0.3661	-	0.4134
B	9.60	-	11.60	0.3780	-	0.4567
C	8.20	-	9.50	0.3228	-	0.3740
D	4.00	-	5.00	0.1575	-	0.1969
E	1.00	-	1.70	0.0394	-	0.0669
F	0.20	-	0.70	0.0079	-	0.0276
G	4.45	-	5.25	0.1752	-	0.2067
H	1.25	-	1.85	0.0492	-	0.0728
J	1.00	-	1.60	0.0394	-	0.0630
K	0.60	-	1.00	0.0236	-	0.0394
L	2.25	-	2.80	0.0886	-	0.1102

RECOMMEDNED LAYOUT DRAWINGS

TO-263



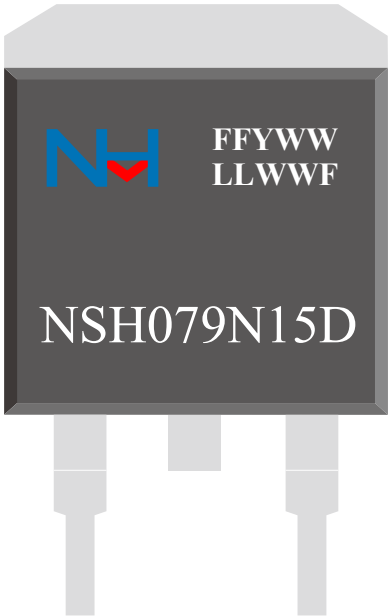
OUTLINE DIMENSIONS						
Dim.	Milimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	-	10.86	-	-	0.4276	-
B	-	7.86	-	-	0.3094	-
C	-	11.64	-	-	0.4583	-
D	-	5.08	-	-	0.2000	-
E	-	3.62	-	-	0.1090	-
F	-	1.14	-	-	0.0560	-

NSH079N15D

N-Channel Enhanced Shielded Gate Trench Power MOSFET



MARKING INFORMATION



MARKING INSTRUCTIONS

NH=Niuhang Trademark
FF=Product Line Code,According To Actual Changes
YWW=Date Code,According To Actual Changes
LLWWF=Inernal Code,According To Actual Changes
NSH079N15D=Model

PACKING INFORMATION

Package Type	Package Code	Product Weight Approx(g/Pcs)	Package Method	Quantity (Pcs/Min. Pack.)	Quantity (Pcs/Inner Box)	Quantity (Pcs/Carton)
TO-263	P1	1.4331	13" Reel	800	800	4000
TO-263	P2	1.4331	13" Reel	800	800	4800

NSH079N15D

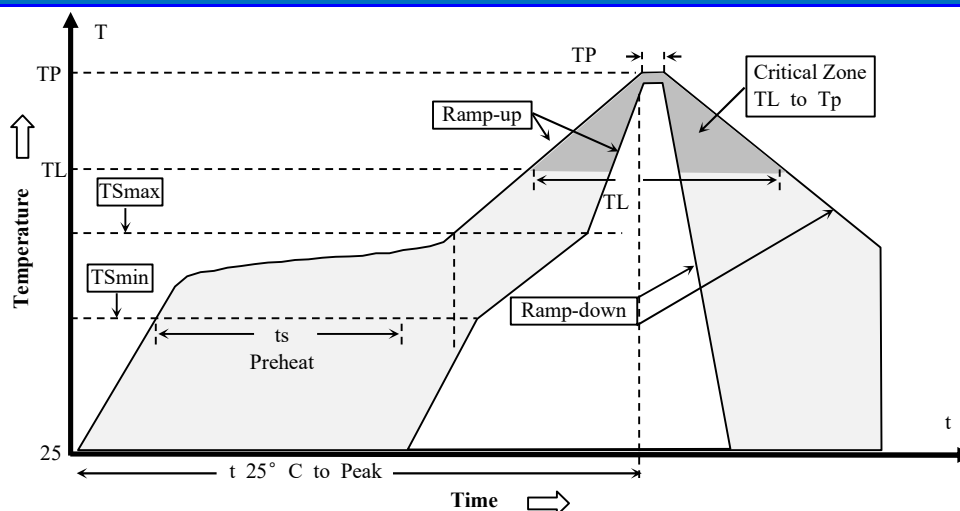
N-Channel Enhanced Shielded Gate Trench Power MOSFET



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat -Temperature Min(T _S min) -Temperature Max(T _S max) -Time(t _s min to t _s max)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
Time maintained above: -Temperature (T _L) - Time (t _L)	183°C 60-150 seconds	217°C 60-150 seconds
Peak Temperature(T _P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

NSH079N15D

N-Channel Enhanced Shielded Gate Trench Power MOSFET



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