

BTA41T-800B



FOUR-QUADRANTS TRIAC

VOLTAGE: 800 Volts

CURRENT: 41 Amperes

Package: TO-3P

FEATURES

- Highly di/dt For High Reliability
- Low Thermal Resistance Insulation Ceramic For Insulated Package
- High Commutation Capability
- Packages Are RoHS (2011/65/EU) Compliant

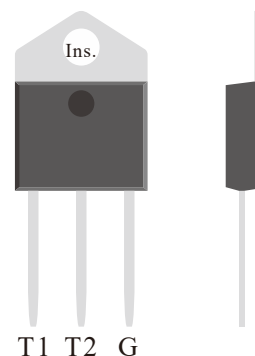
TYPICAL APPLICATIONS

- Phase Control Operation Of Light Dimmers And Motor Speed Controllers
- ON/OFF Control Operation Of Heating Regulation And Induction Motor

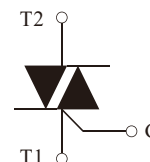
PRODUCT SUMMARY

Gear	B	Unit
$I_{GT(I-II-III)} \text{ Max.}$	50	mA
$I_{GTIV} \text{ Max.}$	70	
$V_{DRM}/V_{RRM} \text{ Min.}$	800	V
$I_{T(RMS)}$	41	A

Diagram:



Polarity:



Absolute Maximum Ratings (Ta=25°C Unless otherwise specified)

Parameter	Test Conditions	Symbol	Ratings	Unit
Repetitive Peak Off-State Voltage		V_{DRM}	800	V
Repetitive Peak Reverse Voltage		V_{RRM}	800	V
RMS On-State Current	$T_c \leq 81^\circ\text{C}$	$I_{T(RMS)}$	41.0	A
Non Repetitive Surge Peak On-State Current	F= 50 Hz, $T_p = 20 \text{ ms}$	I_{TSM}	420	A
	F= 60 Hz, $T_p = 16.6 \text{ ms}$		461	
I^2T Value For Fusing	$T_p = 10 \text{ ms}$	I^2t	1000	A
Critical Rate Of Rise Of On-State Current	$I_G = 2 \cdot I_{GT}$, $t_r \leq 100 \text{ ns}$	di/dt	50	A/ μs
	F= 100 Hz, $T_j = 125^\circ\text{C}$			
Peak Gate Current	$T_p = 20 \mu\text{s}$, $T_j = 125^\circ\text{C}$	I_{GM}	8.0	A
Average Gate Power Dissipation	$T_j = 125^\circ\text{C}$	$P_{G(AV)}$	1.0	W
Maximum Mounting Torque	M3 screw	T_{MM}	1.1	N.m

Thermal Characteristics (Ta=25°C Unless otherwise specified)

Parameter	Test Conditions	Symbol	Typ.	Unit
Operating Junction Temperature Range		T_j	-40 to 125	°C
Storage Temperature Range		T_{STD}	-40 to 150	
Thermal Resistance Junction To Ambient With Steady-State	Still Air Environment With $T_a = 25^\circ\text{C}$	$R_{\theta JA}$	50.0	°C/W
Thermal Resistance Junction-Case With Steady-State	Device Mounted On 75mm x 45mm x 2.5mm Alu. Heat.	$R_{\theta JC}$	0.85	

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Electrical Characteristics (Ta=25°C Unless otherwise specified)

Parameter	Test Conditions	Symbol	Quadrant	Value		Unit
				Gear	B	
Gate Trigger Current	$V_D=12V, R_L=33\Omega$	I_{GT}	I-II-III IV	Max.	50 70	mA
Max. Gate Trigger Voltage	$V_D=12V, R_L=33\Omega$	V_{GT}	ALL	Max.	1.3	V
Min. Gate Trigger Voltage	$V_D=V_{DRM}, T_j=125^\circ C, R_L=3.3K\Omega$	V_{GD}	ALL	Min.	0.2	V
Holding Current	$I_T=100\text{ mA}$	I_H		Max.	50	mA
Latching Current	$I_G=1.2 \cdot I_{GT}$	I_L	I-III-IV II	Max.	80 160	mA
Rise Of Off- State Voltage	$V_D=V_{DRM}, T_j=125^\circ C, \text{Gate Open}$	dv/dt		Min.	1200	V/us

Static Characteristics (Ta=25°C Unless otherwise specified)

Parameter	Test Conditions	Symbol	T_j	Value		Unit
Peak On-State Voltage	$I_T=60\text{ A}$	V_{TM}	25°C	Max.	1.55	V
Threshold Voltage		V_{TO}	125°C	Max.	0.85	V
Dynamic resistance		R_D	125°C	Max.	10	mΩ
Peak Repetitive Forward Blocking Current	$V_D=V_{DRM}$	I_{DRM}	25°C	Max.	5	uA
	$V_D=V_{DRM}$		125°C		5.0	mA
Peak Repetitive Reverse Blocking Current	$V_R=V_{RRM}$	I_{RRM}	25°C	Max.	5	uA
	$V_R=V_{RRM}$		125°C		5.0	mA

Model and identification instructions

	BT	A	41	T	-	800	B
Productur Type							
BT=Triac							
Insulation Spec.:							
A=Ins.=Insulation							
B=No Ins.=No Insulation							
$I_{T(RMS)}$ Spec.:							
41=41A							
Package Spec.:							
T=TO-3P							
"-."=Connection Symbol							
Constant And Unchanging							
V_{DRM}/V_{RRM} Spec.							
800 $\cong$ 800V							
I_{GT} Spec.							
B:IGTI-II-III $\cong$ 50mA;IGTIV $\cong$ 70mA							

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Typical Characteristics Curves

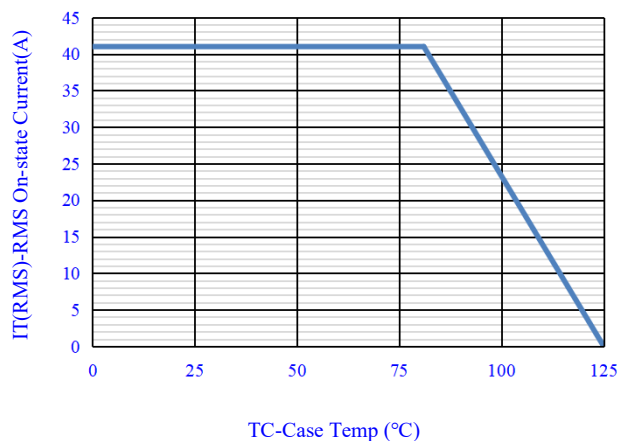


Fig.1-RMS on-state current vs. case temperature

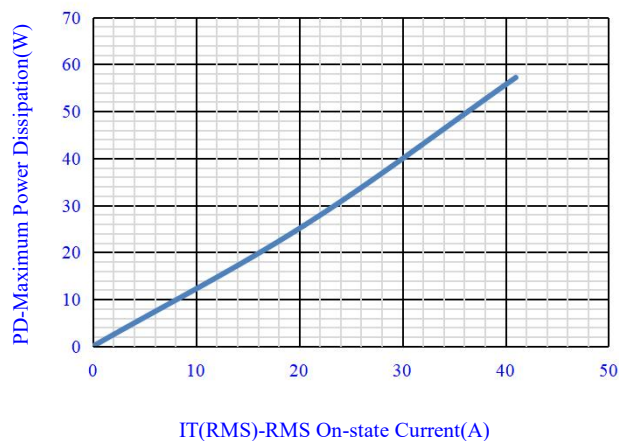


Fig.2- Maximum Power Dissipation Vs. on-state current

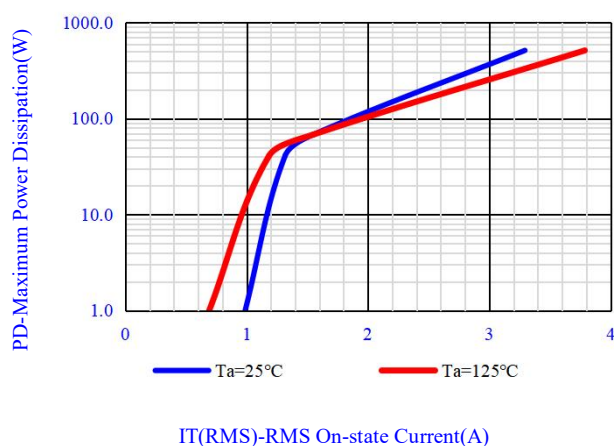


Fig.3- Maximum Power Dissipation Vs. on-state current

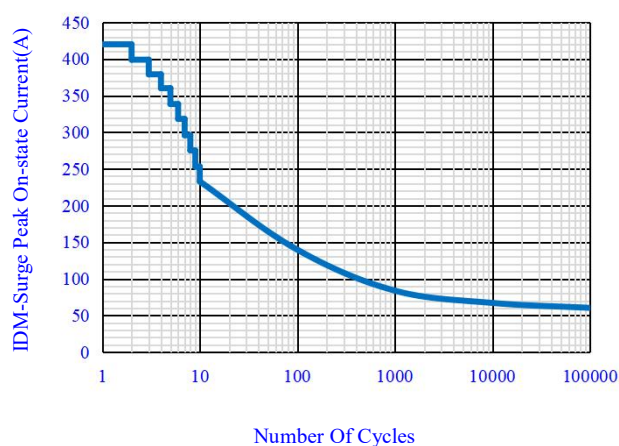


Fig.4- Surge Peak On-state Current Vs. Number Cycles

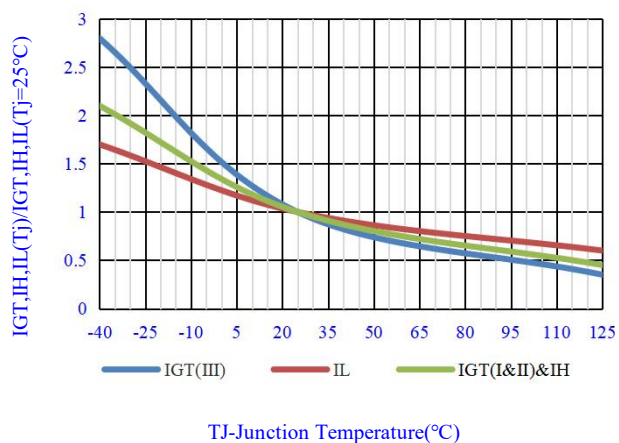


Fig.5- Relative Variation Of Gate Trigger Current , Holding Current And Latching Current Versus Junction Temperature (Typical Value)

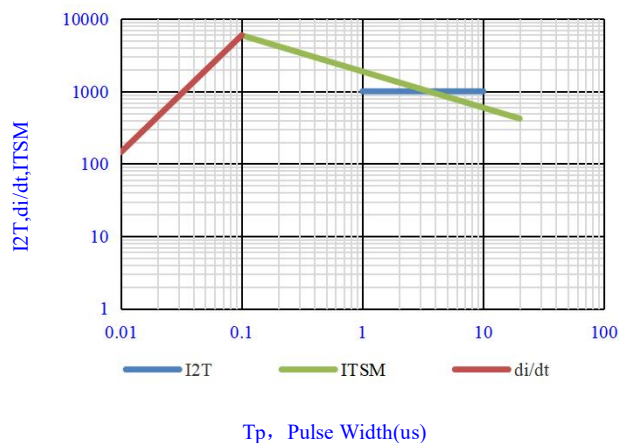
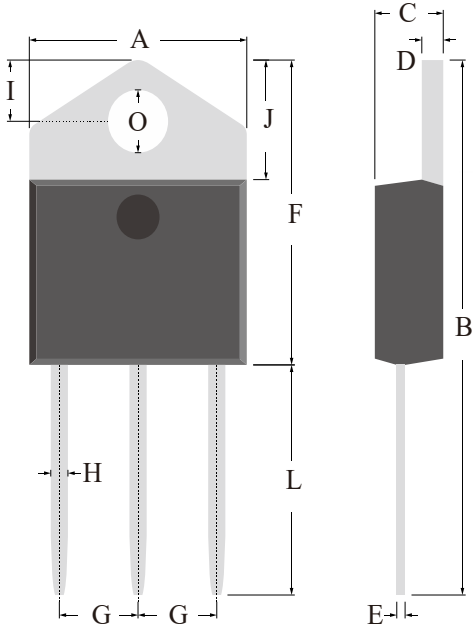


Fig.6- Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$, and corresponding value of I_2t ($di/dt < 50\text{A}/\mu\text{s}$)

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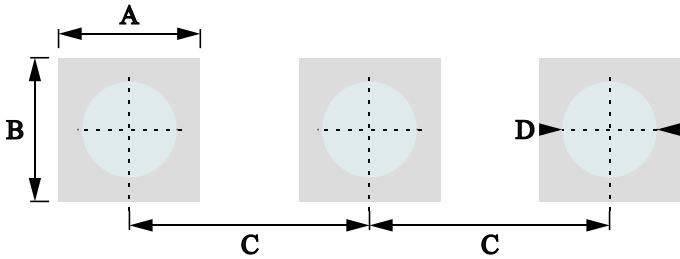
OUTLINE DRAWINGS



TO-3P

OUTLINE DIMENSIONS						
Dim.	Milimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	14.70	-	15.70	0.5787	-	0.6181
B	35.00	-	37.00	1.3780	-	1.4567
C	4.30	-	4.70	0.1693	-	0.1850
D	1.40	-	1.70	0.0551	-	0.0669
E	0.45	-	0.75	0.0177	-	0.0295
F	20.20	-	20.80	0.7953	-	0.8189
G	5.20	-	5.80	0.2047	-	0.2283
H	1.10	-	1.40	0.0433	-	0.0551
I	4.10	-	4.70	0.1614	-	0.1850
J	7.80	-	8.30	0.3071	-	0.3268
L	14.50	-	15.50	0.5709	-	0.6102
O	3.80	-	4.40	0.1496	-	0.1732

RECOMMEDND LAYOUT DRAWINGS



TO-3P

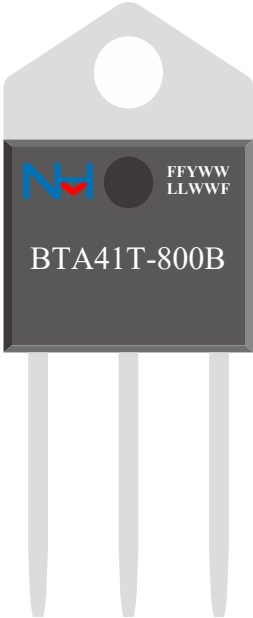
OUTLINE DIMENSIONS						
Dim.	Milimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	-	2.20	-	-	0.0866	-
B	-	2.20	-	-	0.0866	-
C	-	5.50	-	-	0.2165	-
D	-	1.40	-	-	0.0551	-

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MARKING INFORMATION



MARKING INSTRUCTIONS

NH=Niuhang Trademark
FF=Product Line Code,According To Actual Changes
YWW=Date Code,According To Actual Changes
LLWWF=Inernal Code,According To Actual Changes
BTA41T-800B=Model

PACKING INFORMATION

Package Type	Package Code	Product Weight Approx(g/Pcs)	Package Method	Quantity (Pcs/Min. Pack.)	Quantity (Pcs/Inner Box)	Quantity (Pcs/Carton)
TO-3P	P1	6.6474	Tube	30	450	2700
TO-3P	P1	6.6474	Tube	30	600	3000
TO-3P	P1	6.6474	Tube	30	600	3600

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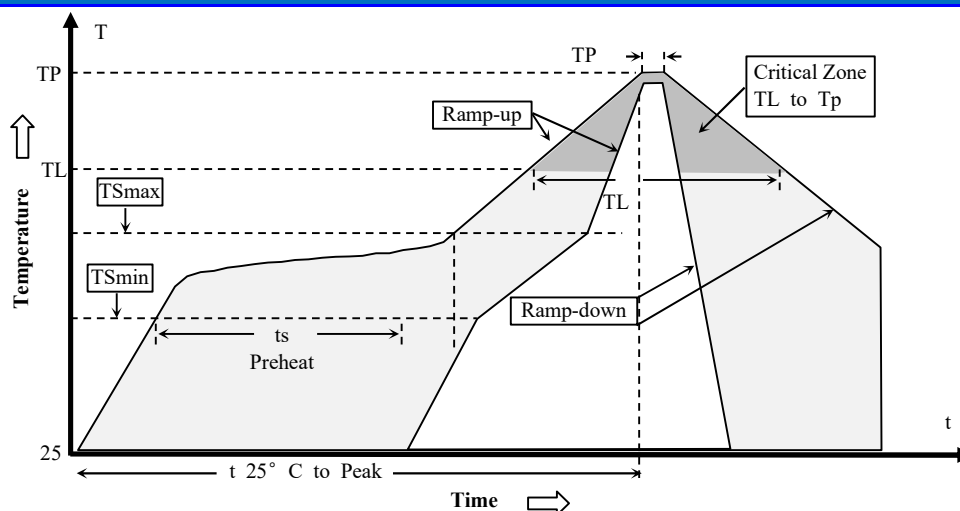
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Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat -Temperature Min(TS min) -Temperature Max(TS max) -Time(ts min to ts max)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
Time maintained above: -Temperature (TL) - Time (tL)	183°C 60-150 seconds	217°C 60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

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