

NPS18N50D

N-Channel Enhancement Mode Power MOSFET



Voltage: 500 Volts

Current: 18.0 Ampers

Package: TO-263

Features

- NH'S Advanced Planar DMOS Technology
- Low Rds(on) For Low On-State Loss
- High EAS For High Reliability
- Qualified According To JEDEC Criteria

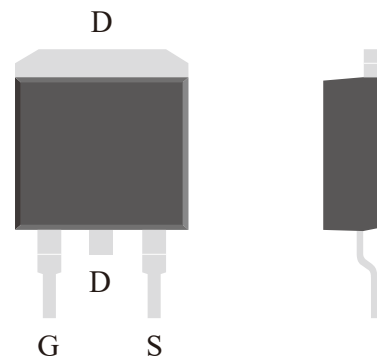
Typical Applications

- AC/DC Converter
- Adaptor And Charger
- PC Power
- LED Drives And LED Lighting

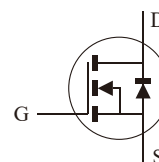
Product Summary

VDS Min.@Tj	500	V
Id Min.@Ta	18.0	A
RDS(ON)(TYP)@10V	0.29	Ω

Diagram:



Polarity:



***100% UIS TESTED**

***100% ΔVDS TESTED**

Absolute Maximum Ratings (Ta=25℃ Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Ratings	Unit
Drain-Source Voltage		V _{DS}	500	V
Gate-Source Voltage		V _{GS}	±30	V
Continuous Drain Current (Note 1)	Ta= 25 ℃	I _D	18.0	A
	Ta= 100 ℃		11.5	
Drain Current-Pulse (Note 1)	Tj< 150 ℃	I _{DM}	72	A
Maximum Power Dissipation Power Dissipation Derating Factor Above 25℃	Ta= 25 ℃	P _D	192	W
	Ta= 100 ℃		77	
Derating Factor		D _F	1.54	W/℃
Avalanche Current,Single Pulse (Note 1)	L= 10.0 mH	I _{AS}	14.0	A
Single Pulse Avalanche Energy (Note 1) Test Circuit & Waveform See Fig.16	L= 10.0 mH IAS= 14.0 A,RG= 10.0 Ω Starting Tj=25 ℃,VG= 10.0 V	E _{AS}	980	mJ

Thermal Characteristics (Ta=25℃ Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Typ.	Unit
Junction Temperature		T _J	-55 to 150	℃
Storage Temperature Range		T _{STD}	-55 to 150	℃
Thermal Resistance Junction To Ambient With Steady-State	Still Air Environment With Ta=25℃	R _{θJA}	62.5	℃/W
Thermal Resistance Junction-Case With Steady-State	Device Mounted On 1 in2 FR-4 Board With 2oz. Copper	R _{θJC}	0.65	

Notes: 1.Pulse Width Limited By Max. Junction Temperature. (See Fig. 13).

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Electrical Characteristics (Ta=25°C Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Static Off Characteristics						
Drain-Source Breakdown Voltage	VGS=0V,ID=250uA	BV _{DSS}	500	--	--	V
Bvdss Temperature Coefficient	ID=250uA,Reference25℃	ΔBV _{DSS} /ΔT _J	--	0.59	--	V/℃
Drain-Source Leakage Current	VDS= 500 V,VGS=0V	I _{DSS}	--	--	1	uA
Gate-Body Leakage Current	VGS= ±30 V,VDS=0V	I _{GSS}	--	--	±100	nA
Forward Transconductance	ID= 9.0 A,VDS= 15 V	g _{fs}	--	16.0	--	S
Static On Characteristics						
Gate Threshold Voltage	VGS= VDS ID=250uA	V _{GS(TH)}	2.0	3.0	4.0	V
Drain-Source On Resistance	ID= 9.0 A,VGS= 10.0 V	R _{DS(ON)}	--	0.29	0.35	Ω
	ID= 9.0 A,VGS= 4.5 V		--	0.38	0.53	
Dynamic Characteristics						
Input Capacitance	VDS= 50 V	C _{iss}	--	2620.0	--	pF
Output Capacitance	VGS= 0 V	C _{oss}	--	220.0	--	pF
Reverse Transfer Capacitance	F= 1 MHZ	C _{rss}	--	6.0	--	pF
Switching Paramters (Test Circuit & Waveform See Fig.14)						
Turn-On Delay Time	VDS= 250 V	t _{d(on)}	--	28.0	--	ns
Turn-On Rise Time	VGS= 10.0 V	t _r	--	47.0	--	ns
Turn-Off Delay Time	RG= 10.0 Ω	t _{d(off)}	--	57.0	--	ns
Turn-Off Rise Time		t _f	--	40.0	--	ns
Gate Charge Paramters (Test Circuit & Waveform See Fig.15)						
Total Gate Charge	VDS= 250 V	Q _g	--	46.0	--	nC
Gate-Source Charge	VGS= 10.0 V	Q _{gs}	--	12.6	--	nC
Gate-Drain Charge	ID= 9.0 A	Q _{gd}	--	15.7	--	nC
Drain-Source Diode Characteristics And Maximum Ratings (Test Circuit & Waveform See Fig.17)						
Max. Diode Forward Cuurent		I _s	--	--	18.0	A
Max. Pulsed Forward Cuurent		I _{SM}	--	--	72	A
Diode Forward Voltage	ID= 9.0 A,VGS=0V	V _{SD}	--	0.85	1.5	V
Reverse Recovery Time	ID= 9.0 A,di/dt= 100 A/us	t _{rr}	--	450.0	--	ns
Reverse Recovery Charge	VGS= 10.0 V,VDD= 250 V	Q _{rr}	--	4760.0	--	nC

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Typical Characteristics Curves

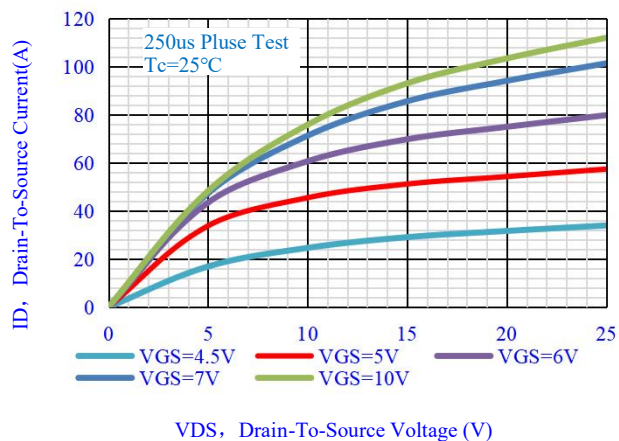


Fig.1-Output Characteristics

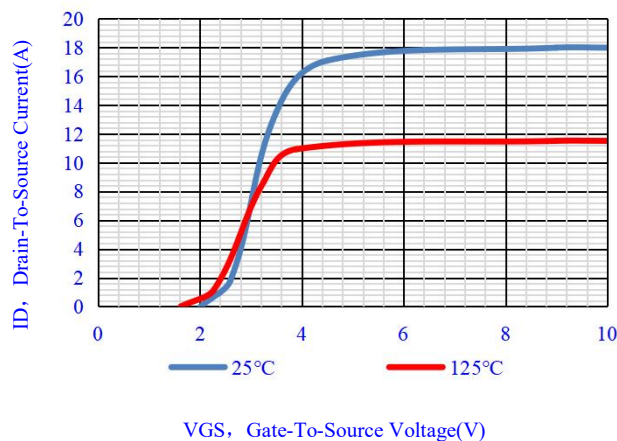


Fig.2- Transfer Characteristics

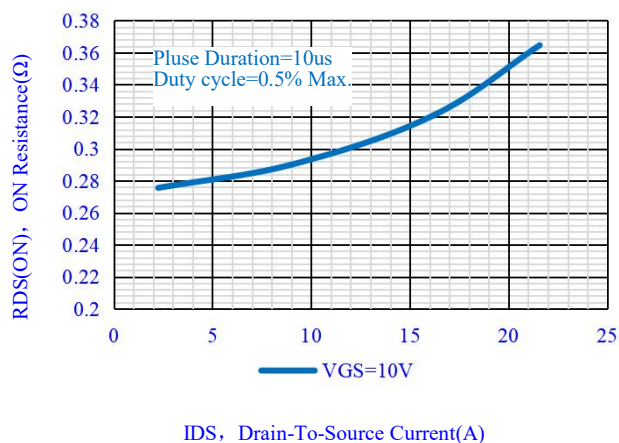


Fig.3- On Resistance vs. Drain Current

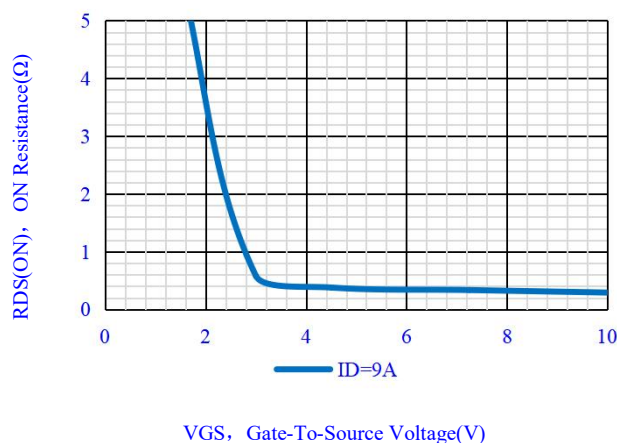


Fig.4- On Resistance vs. Gate Source Voltage

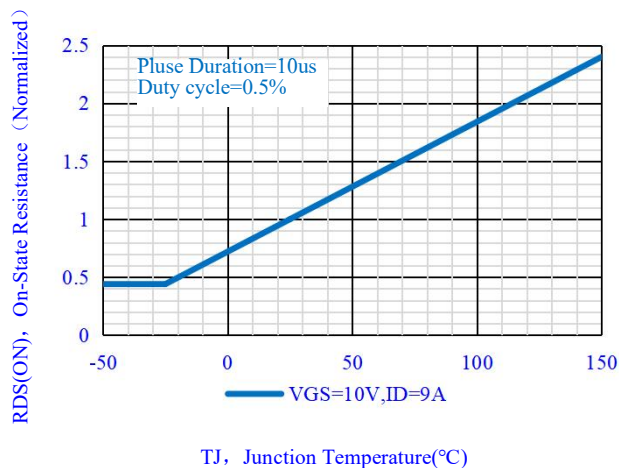


Fig.5- On Resistance vs. Junction Temperature

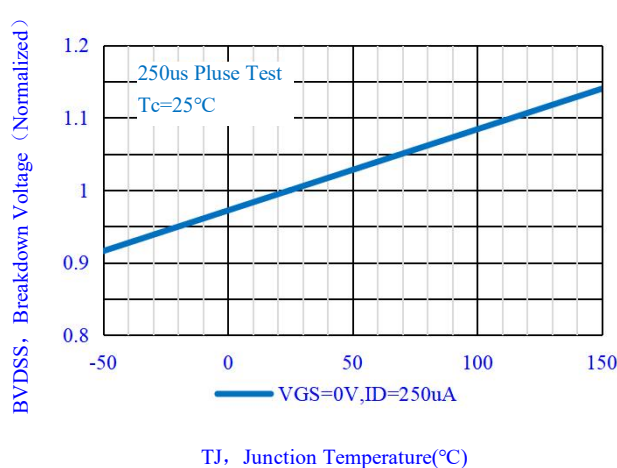


Fig.6- Breakdown Voltage vs. Junction Temperature

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Typical Characteristics Curves

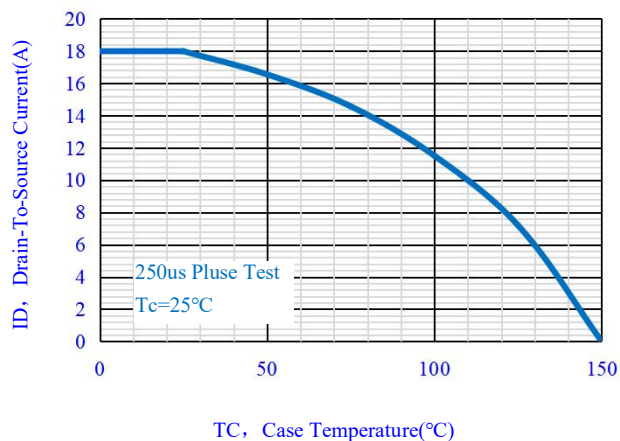


Fig.7-Maximum Continuous Drain Current vs. Case Temperature

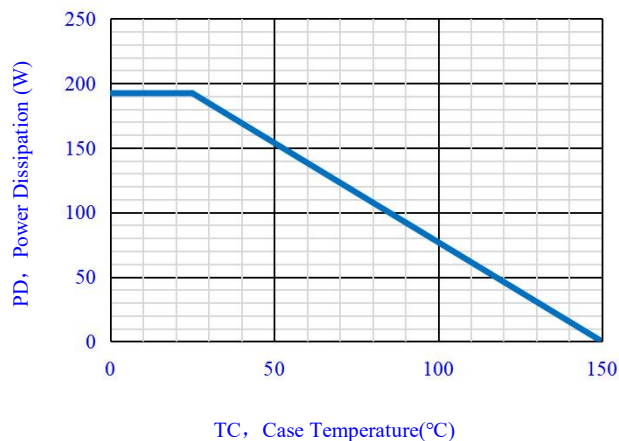


Fig.8-Maximum Power Dissipation vs. Case Temperature

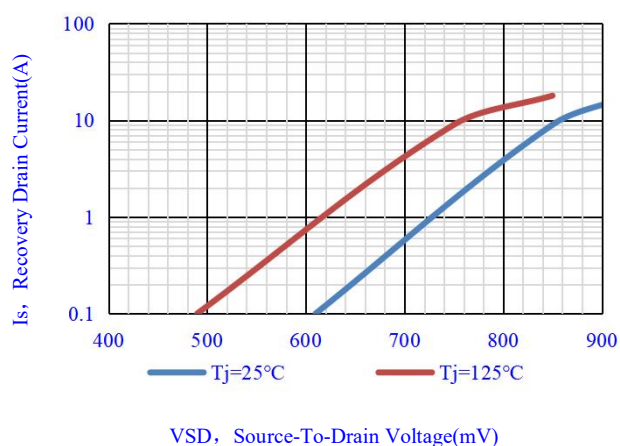


Fig.9- Source-To-Drain Diode Forward Voltage

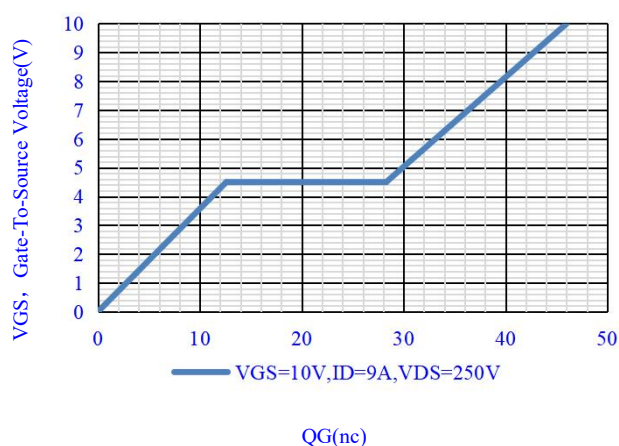


Fig.10-Gate Charge Waveform

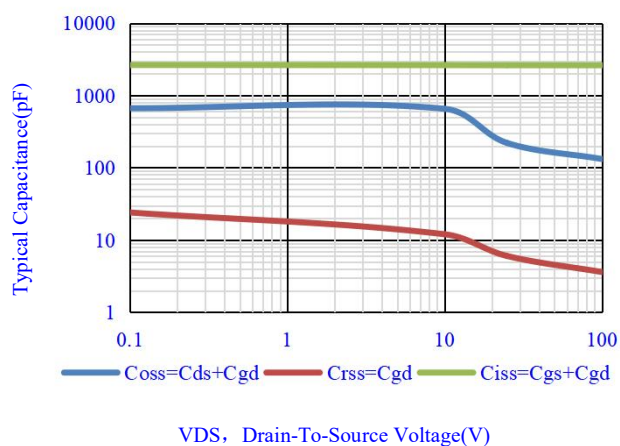


Fig.11-Typical Capacitance vs. Drain-To-Source Voltage

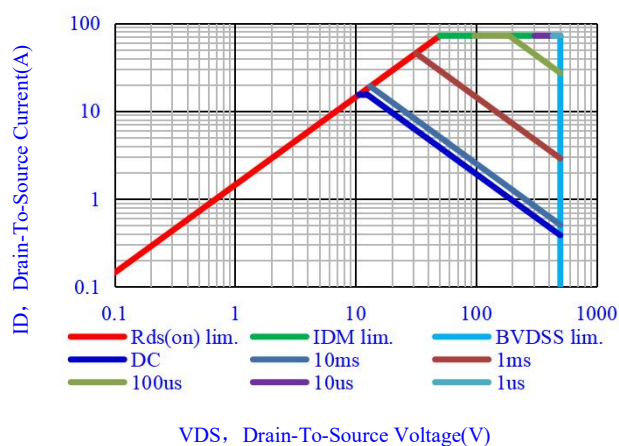


Fig.12-Maximum Safe Operating Area(SOA)

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Typical Characteristics Curves

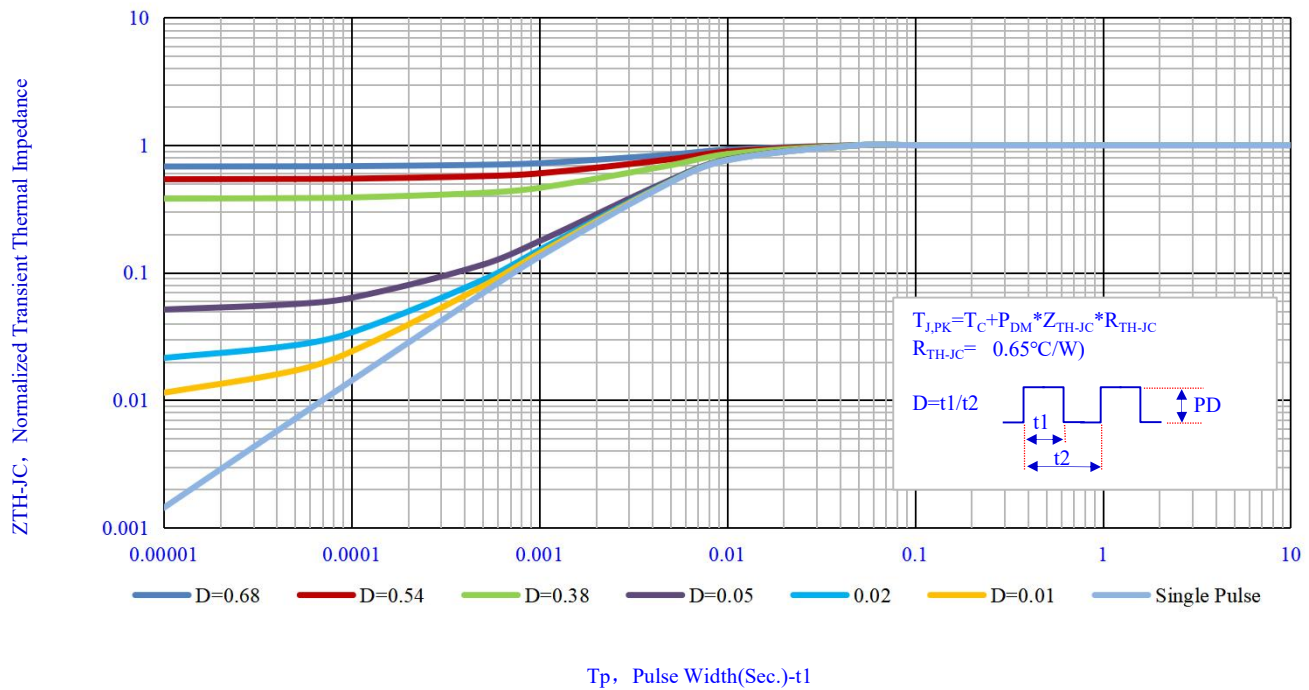


Fig.13- Normalized Maximum Transient Thermal Impedance vs.Pulse Width

Test Circuit & Waveform

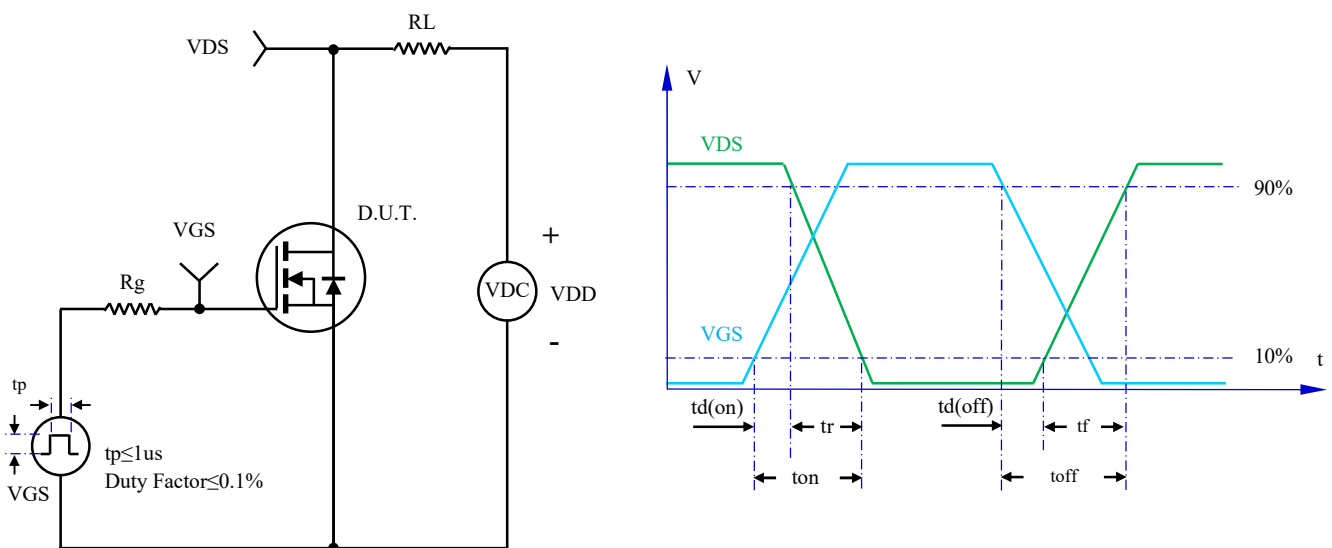


Fig.14- Resistive Switching Test Circuit & Waveform

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Test Circuit & Waveform

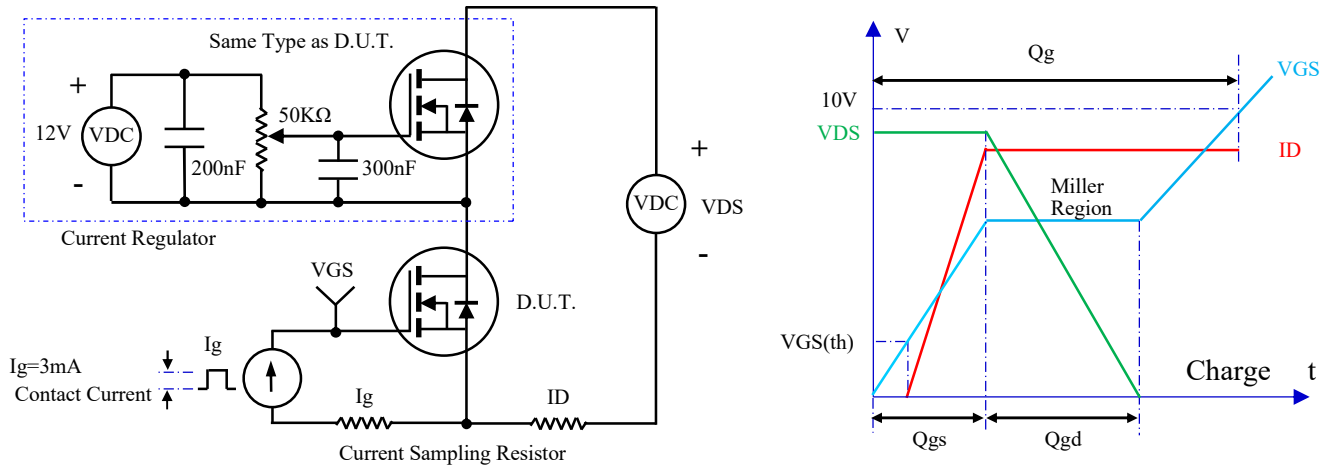


Fig.15- Gate Charge Test Circuit & Waveform

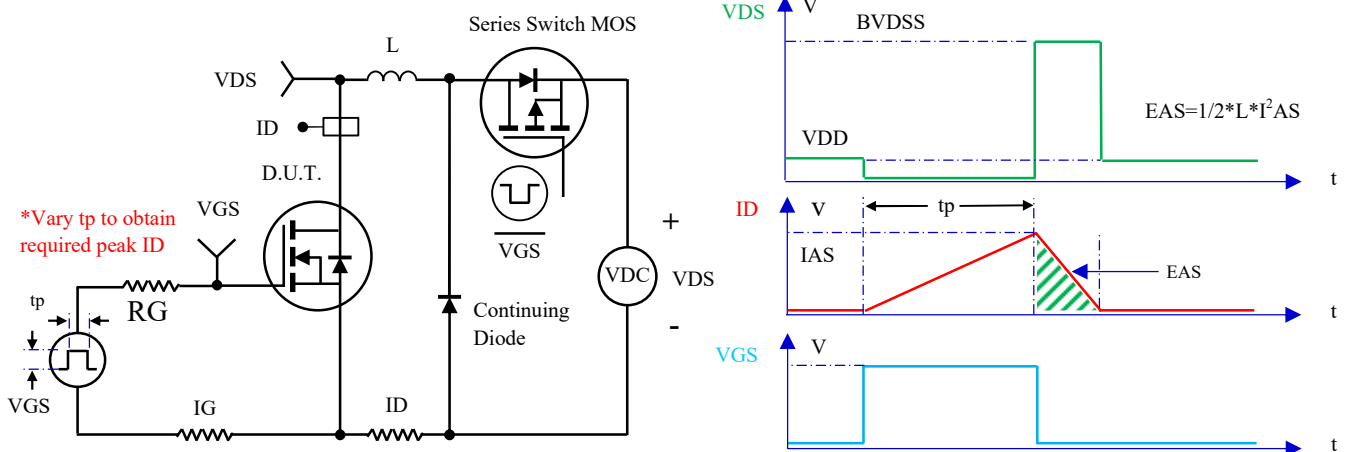


Fig.16- Unclamped Inductive Switching (UIS) Test Circuit & Waveform

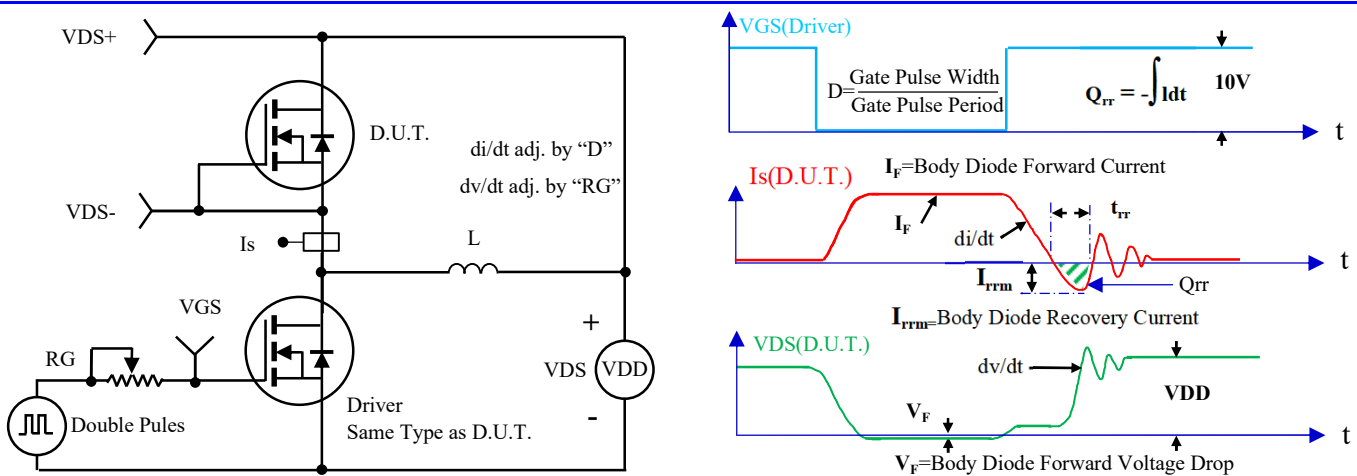


Fig.17- Diode Recovery Test Circuit & Waveform

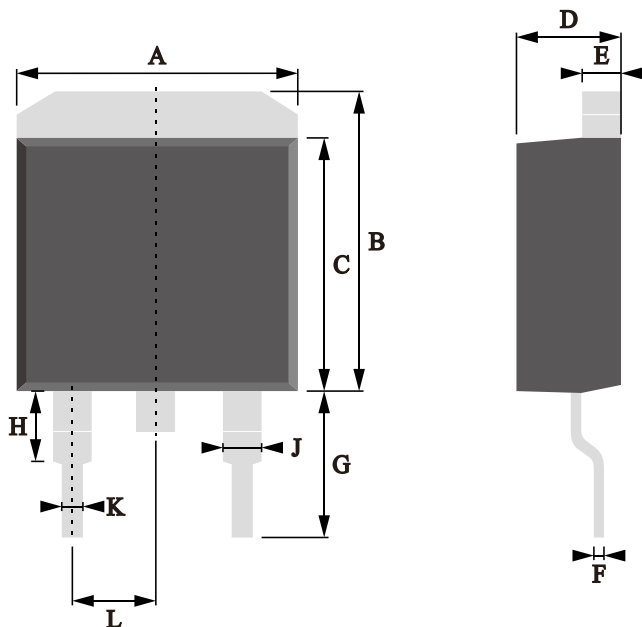
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OUTLINE DRAWINGS

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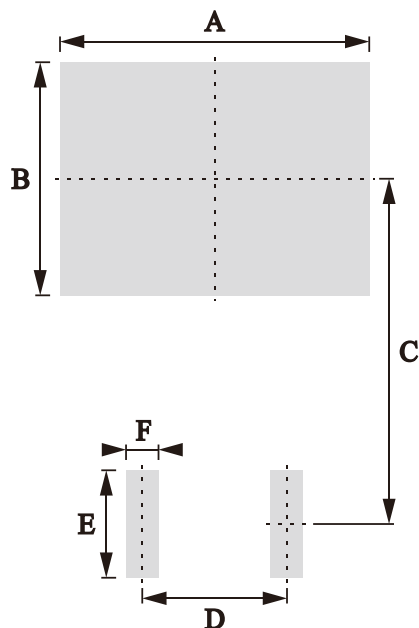


OUTLINE DIMENSIONS

Dim.	Milimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	9.30	-	10.50	0.3661	-	0.4134
B	9.60	-	11.60	0.3780	-	0.4567
C	8.20	-	9.50	0.3228	-	0.3740
D	4.00	-	5.00	0.1575	-	0.1969
E	1.00	-	1.70	0.0394	-	0.0669
F	0.20	-	0.70	0.0079	-	0.0276
G	4.45	-	5.25	0.1752	-	0.2067
H	1.25	-	1.85	0.0492	-	0.0728
J	1.00	-	1.60	0.0394	-	0.0630
K	0.60	-	1.00	0.0236	-	0.0394
L	2.25	-	2.80	0.0886	-	0.1102

RECOMMEDNED LAYOUT DRAWINGS

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OUTLINE DIMENSIONS

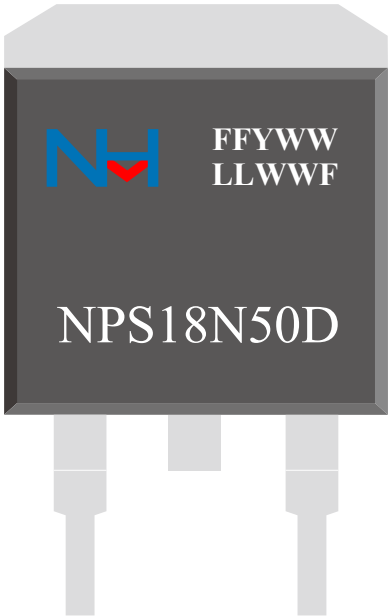
Dim.	Milimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	-	10.86	-	-	0.4276	-
B	-	7.86	-	-	0.3094	-
C	-	11.64	-	-	0.4583	-
D	-	5.08	-	-	0.2000	-
E	-	3.62	-	-	0.1090	-
F	-	1.14	-	-	0.0560	-

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MARKING INFORMATION



MARKING INSTRUCTIONS

NH=Niuhang Trademark
FF=Product Line Code,According To Actual Changes
YWW=Date Code,According To Actual Changes
LLWWF=Inernal Code,According To Actual Changes
NPS18N50D=Model

PACKING INFORMATION

Package Type	Package Code	Product Weight Approx(g/Pcs)	Package Method	Quantity (Pcs/Min. Pack.)	Quantity (Pcs/Inner Box)	Quantity (Pcs/Carton)
TO-263	P1	1.4331	13" Reel	800	800	4000
TO-263	P2	1.4331	13" Reel	800	800	4800

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Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat -Temperature Min(T _S min) -Temperature Max(T _S max) -Time(t _s min to t _s max)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
Time maintained above: -Temperature (T _L) - Time (t _L)	183°C 60-150 seconds	217°C 60-150 seconds
Peak Temperature(T _P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

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