

NSH010N10TL



N-Channel Enhanced Shielded Gate Trench Power MOSFET

Voltage: 100 Volts

Current: 454 Ampers

Package: TOLT

Features

- NH'S Advanced SGT Technology
- Low Reverse Transfer Capacitances
- Low Gate Charge For Low Switching Loss
- Excellent $Q_g \cdot R_{ds(on)}$ Product(FOM)

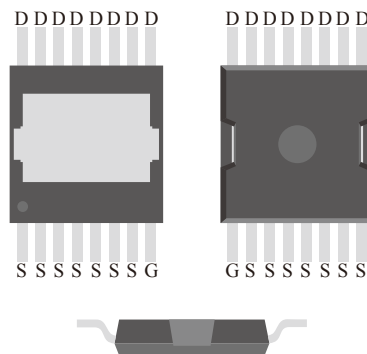
Typical Applications

- Synchronous Rectification
- High-Frequency Circuits
- The Motor Drives
- Automotive Electronics

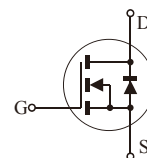
Product Summary

VDS Min.@Tj	100	V
ID Min.@Ta	454	A
RDS(ON)(TYP)@10V	1	mΩ

Diagram:



Polarity:



***100% UIS TESTED**

***100% ΔVDS TESTED**

Absolute Maximum Ratings (Ta=25℃ Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Ratings	Unit
Drain-Source Voltage		V_{DS}	100	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current (Note 1)	Ta= 25 ℃	I_D	454	A
	Ta= 100 ℃		409	
Drain Current-Pulse (Note 1)	Tj< 175 ℃	I_{DM}	1816	A
Maximum Power Dissipation Power Dissipation Derating Factor Above 25℃	Ta= 25 ℃	P_D	536	W
	Ta= 100 ℃		268	
Derating Factor		D_F	3.57	W/℃
Avalanche Current,Single Pulse (Note 1)	L= 3.0 mH	I_{AS}	40	A
Single Pulse Avalanche Energy (Note 1) Test Circuit & Waveform See Fig.17	L= 3.0 mH IAS= 40 A, RG= 10.0 Ω Starting Tj=25 ℃, VG = 10.0 V	E_{AS}	2400	mJ

Thermal Characteristics (Ta=25℃ Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Typ.	Unit
Junction Temperature		T_J	-55 to 175	℃
Storage Temperature Range		T_{STD}	-55 to 175	℃
Thermal Resistance Junction To Ambient With Steady-State	Still Air Environment With Ta=25℃	$R_{θJA}$	40	℃/W
Thermal Resistance Junction-Case With Steady-State	Device Mounted On 1 in2 FR-4 Board With 2oz. Copper	$R_{θJC}$	0.28	

Notes: 1.Pulse Width Limited By Max. Junction Temperature. (See Fig. 14).

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Electrical Characteristics (Ta=25°C Unless Otherwise Specified)

Parameter	Test Conditions	Symbol	Min.	Typ.	Max.	Unit
Static Off Characteristics						
Drain-Source Breakdown Voltage	VGS=0V,ID=250uA	BV _{DSS}	100	--	--	V
Bvdss Temperature Coefficient	ID=250uA,Reference25℃	ΔBV _{DSS} /ΔT _J	--	0.10	--	V/℃
Drain-Source Leakage Current	VDS= 100 V,VGS=0V	I _{DSS}	--	--	1	uA
Gate-Body Leakage Current	VGS= ±20 V,VDS=0V	I _{GSS}	--	--	±100	nA
Forward Transconductance	ID= 20.0 A,VDS= 5 V	g _{fs}	--	63	--	S
Static On Characteristics						
Gate Threshold Voltage	VGS= VDS ID=250uA	V _{GS(TH)}	2.5	3	3.5	V
Drain-Source On Resistance	ID= 20.0 A,VGS= 10.0 V	R _{DS(ON)}	--	1	1.3	mΩ
	ID= 20.0 A,VGS= 5.0 V		--	1	2	
Dynamic Characteristics						
Input Capacitance	VDS= 50 V	C _{iss}	--	10750	--	pF
Output Capacitance	VGS= 0 V	C _{oss}	--	5020	--	pF
Reverse Transfer Capacitance	F= 1 MHZ	C _{rss}	--	90	--	pF
Gate Resistance	VGS=0V,VDS=0V, Freq.=1MHz	R _g	--	4.2	--	Ω
Switching Paramters (Test Circuit & Waveform See Fig.15)						
Turn-On Delay Time	VDS= 50 V	t _{d(on)}	--	40	--	ns
Turn-On Rise Time	VGS= 10.0 V	t _r	--	56	--	ns
Turn-Off Delay Time	RG= 10.0 Ω	t _{d(off)}	--	128	--	ns
Turn-Off Rise Time	L= 3.0 mH	t _f	--	62	--	ns
Gate Charge Paramters (Test Circuit & Waveform See Fig.16)						
Total Gate Charge	VDS= 50 V	Q _g	--	163	--	nC
Gate-Source Charge	VGS= 10.0 V	Q _{gs}	--	52	--	nC
Gate-Drain Charge	ID= 20.0 A	Q _{gd}	--	34	--	nC
Drain-Source Diode Characteristics And Maximum Ratings (Test Circuit & Waveform See Fig.18)						
Max. Diode Forward Cuurent		I _S	--	--	454.0	A
Max. Pulsed Forward Cuurent		I _{SM}	--	--	1816	A
Diode Forward Voltage	ID= 20.0 A,VGS=0V	V _{SD}	--	0.7	1.2	V
Reverse Recovery Time	IS= 20.0 A,di/dt= 100 A/us	t _{rr}	--	135	--	ns
Reverse Recovery Charge	VGS= 0.0 V	Q _{rr}	--	355	--	nC
Reverse Recovery Current		I _{RRM}	--	5.0	--	A

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Typical Characteristics Curves

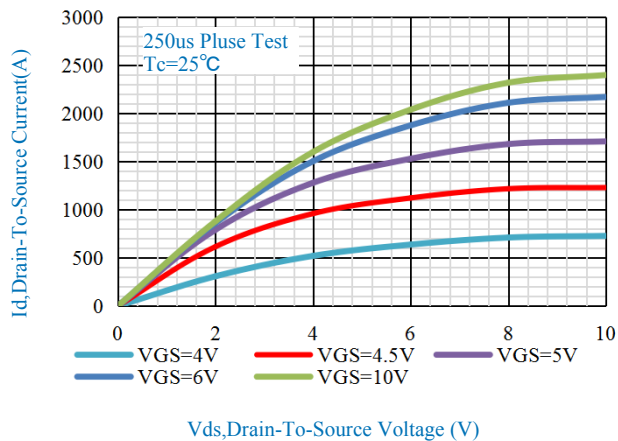


Fig.1-Output Characteristics

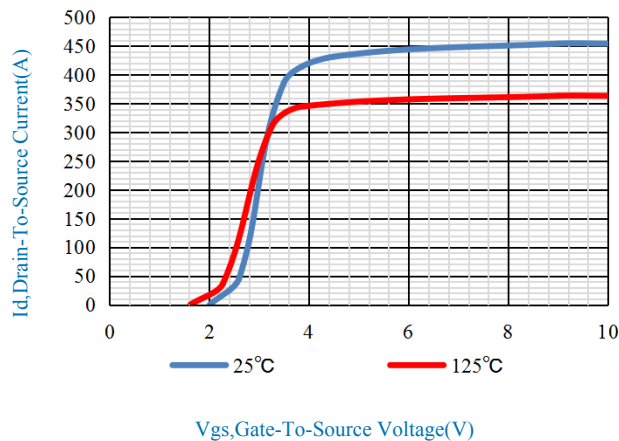


Fig.2-Transfer Characteristics

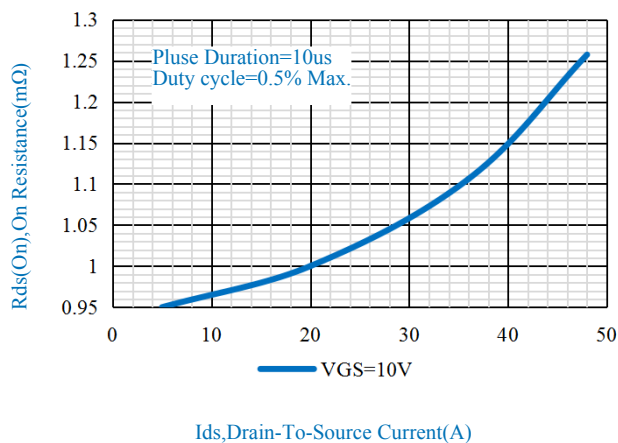


Fig.3-On Resistance Vs. Drain Current

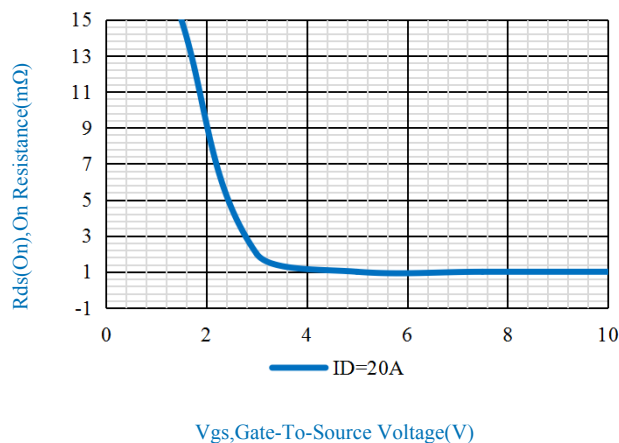


Fig.4-On Resistance Vs. Gate Source Voltage

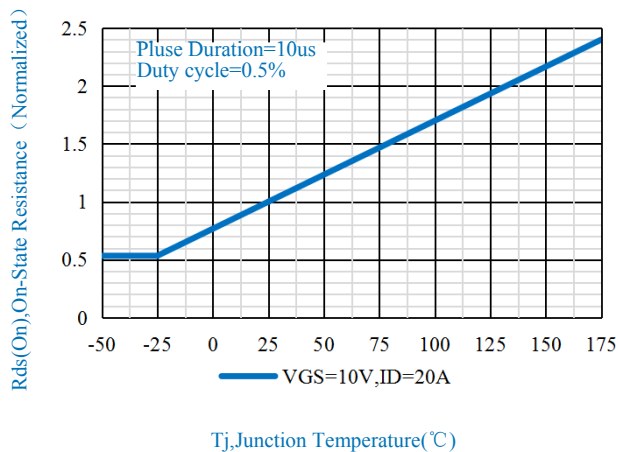


Fig.5-On Resistance Vs. Junction Temperature

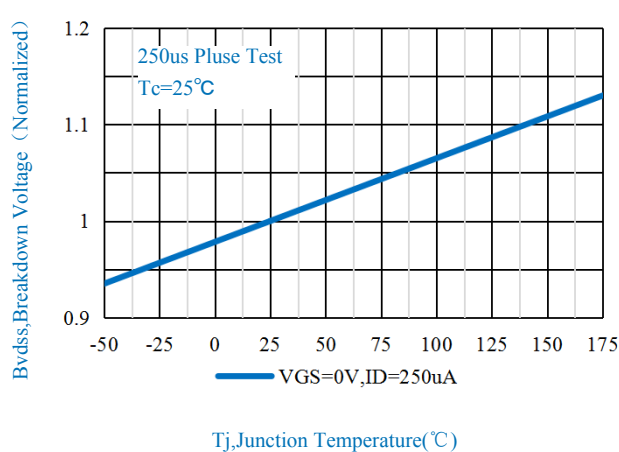


Fig.6-Breakdown Voltage Vs. Junction Temperature

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Typical Characteristics Curves

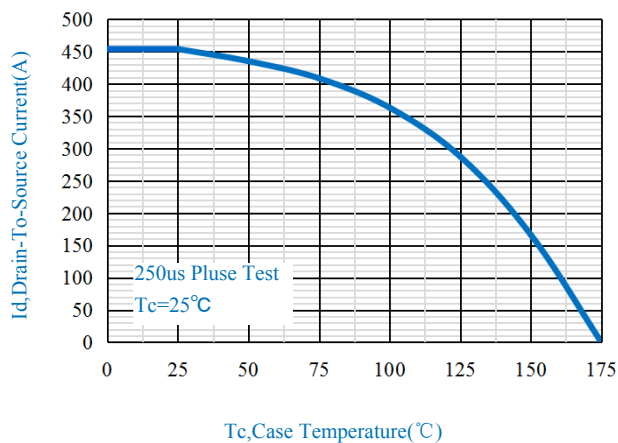


Fig.7-Maximum Continuous Drain Current Vs. Case Temperature

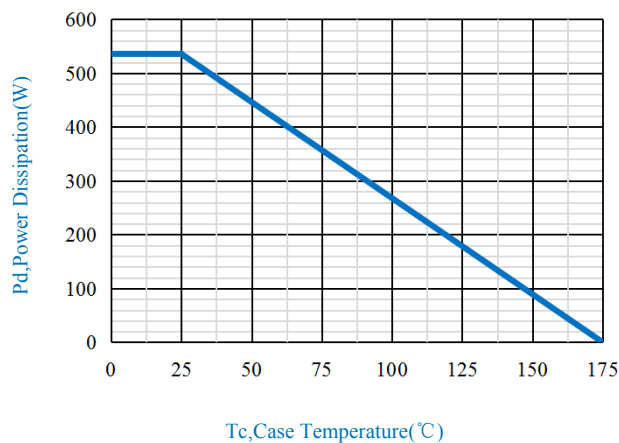


Fig.8-Maximum Power Dissipation Vs. Case Temperature

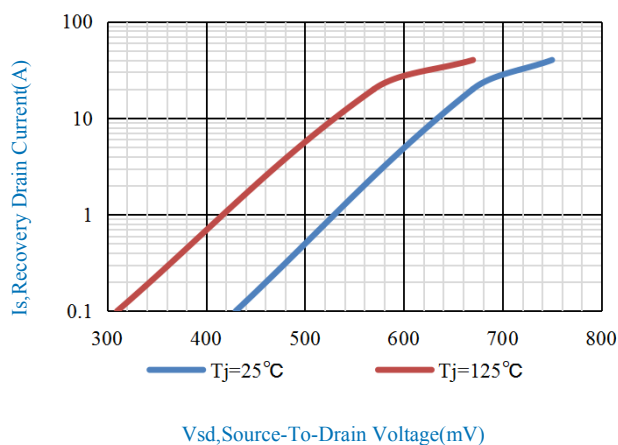


Fig.9-Source-To-Drain Diode Forward Voltage

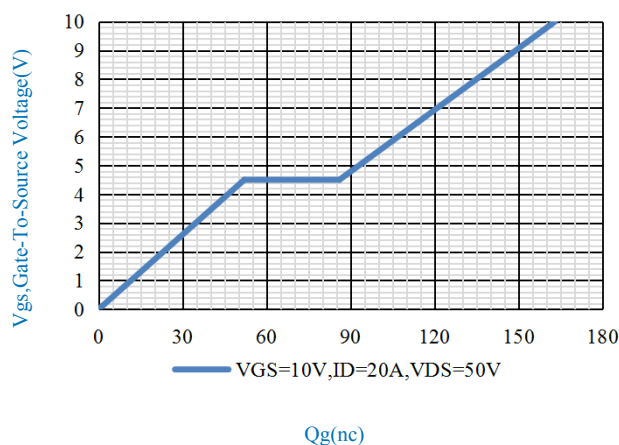


Fig.10-Gate Charge Waveform

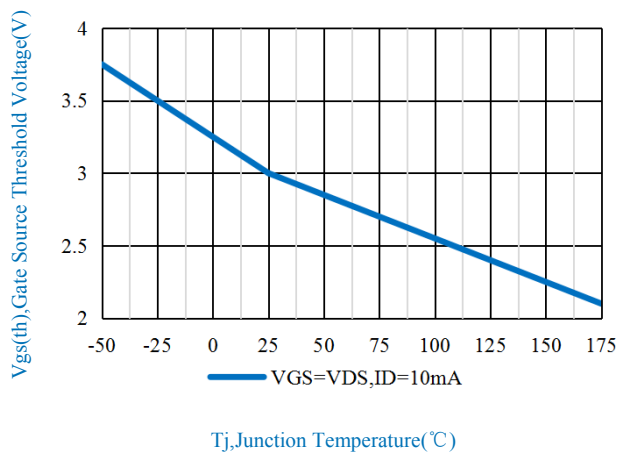


Fig.11-Gate Source Threshold Voltage Vs. Junction Temperature

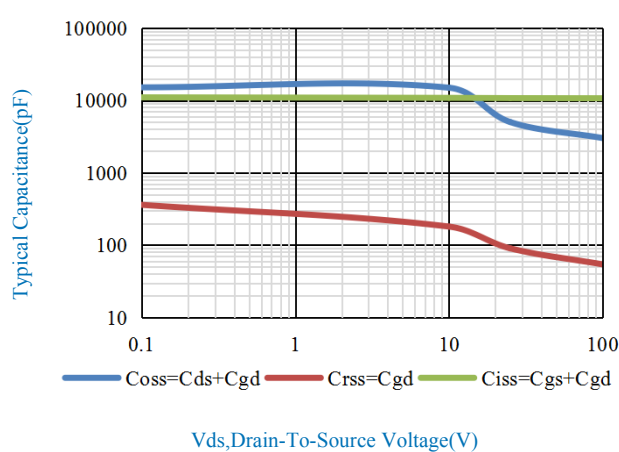


Fig.12-Typical Capacitance Vs. Drain-To-Source Voltage

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Typical Characteristics Curves

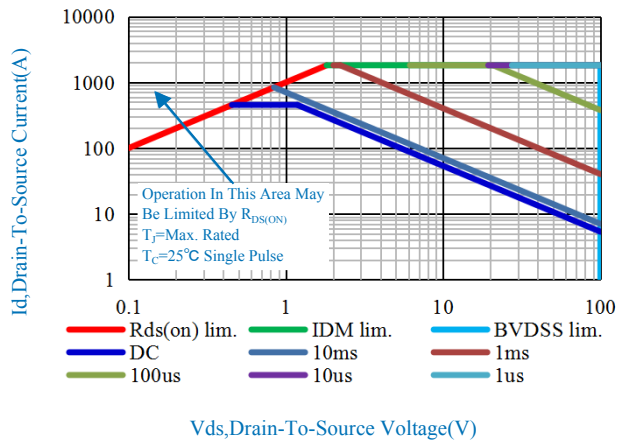


Fig.13-Maximum Safe Operating Area(SOA)

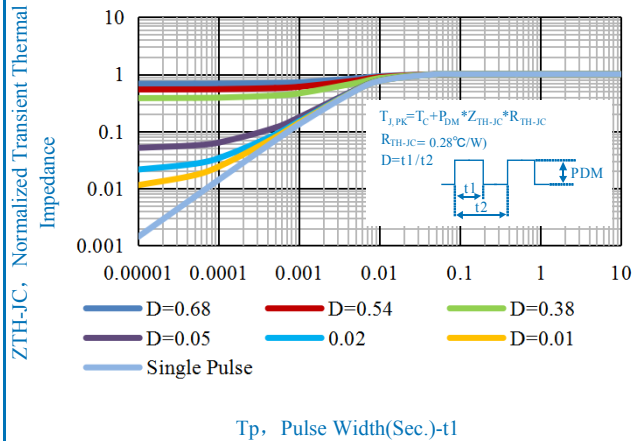


Fig.14- Normalized Maximum Transient Thermal Impedance Vs. Pulse Width

Test Circuit & Waveform

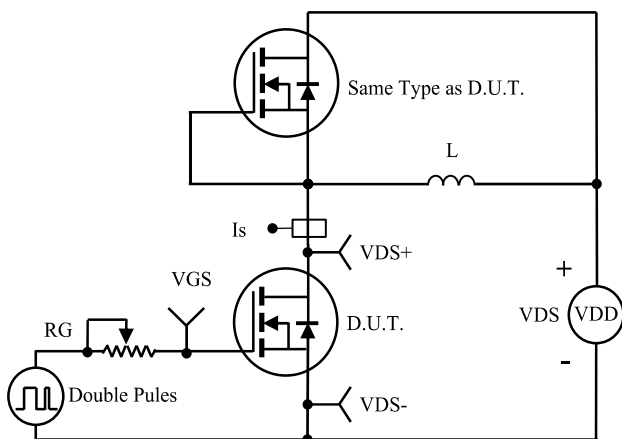


Fig.15- Inductive Switching Test Circuit & Waveform

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Test Circuit & Waveform

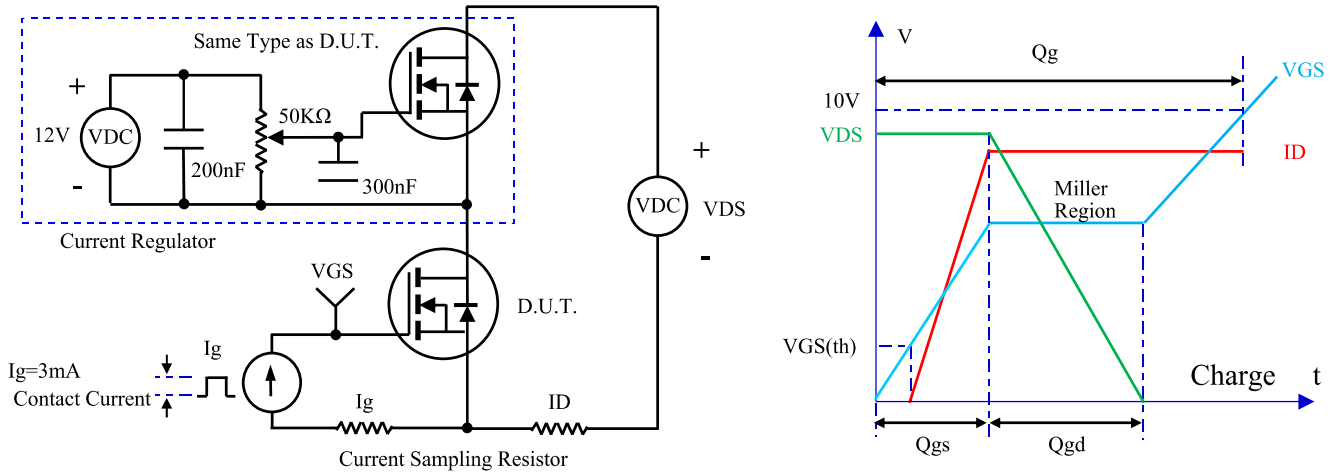


Fig.16- Gate Charge Test Circuit & Waveform

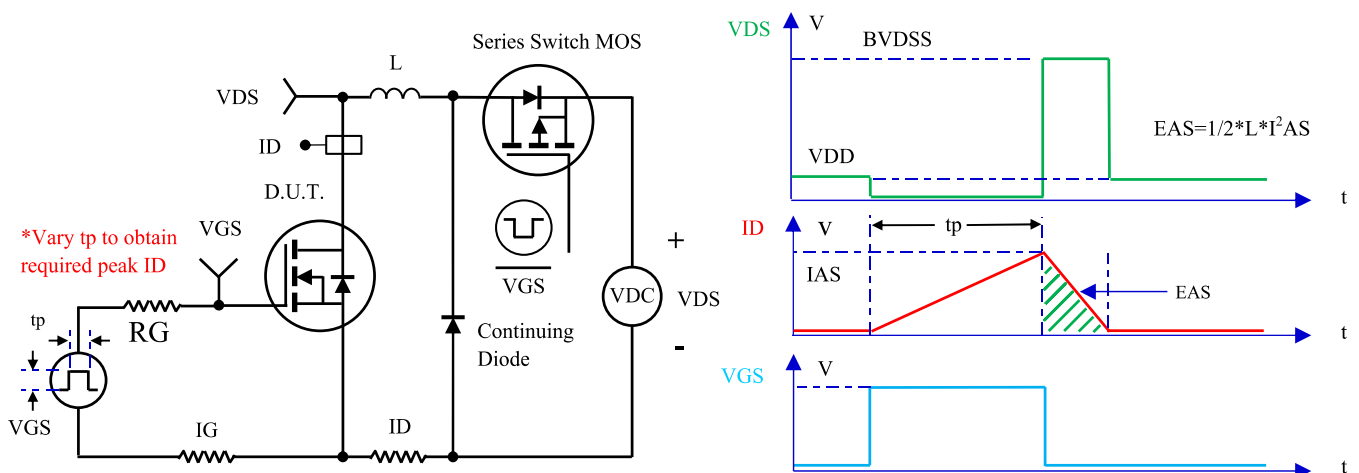


Fig.17- Unclamped Inductive Switching (UIS) Test Circuit & Waveform

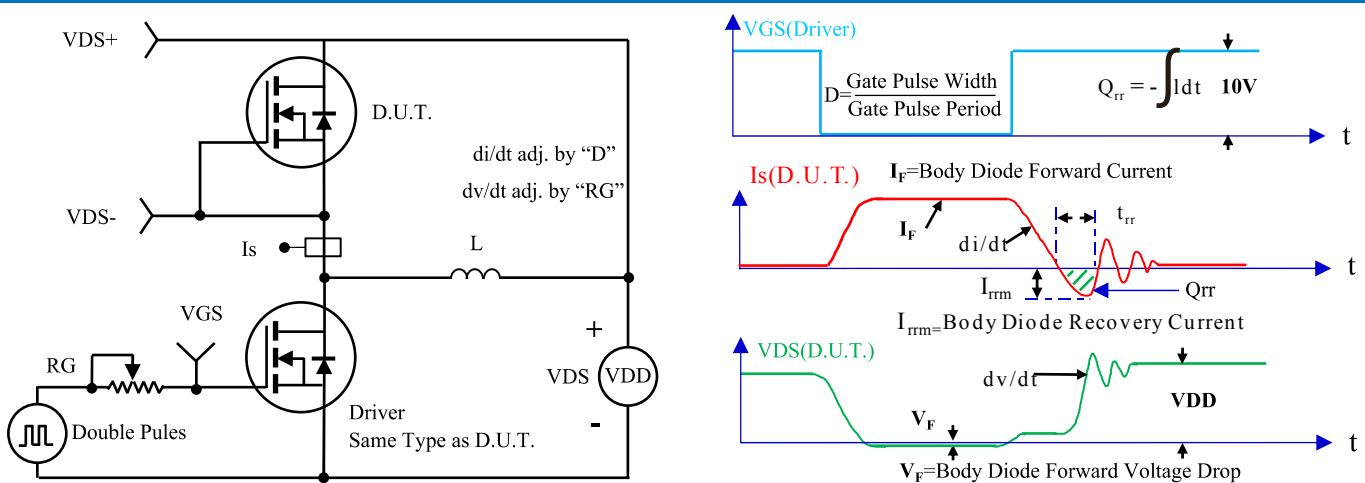


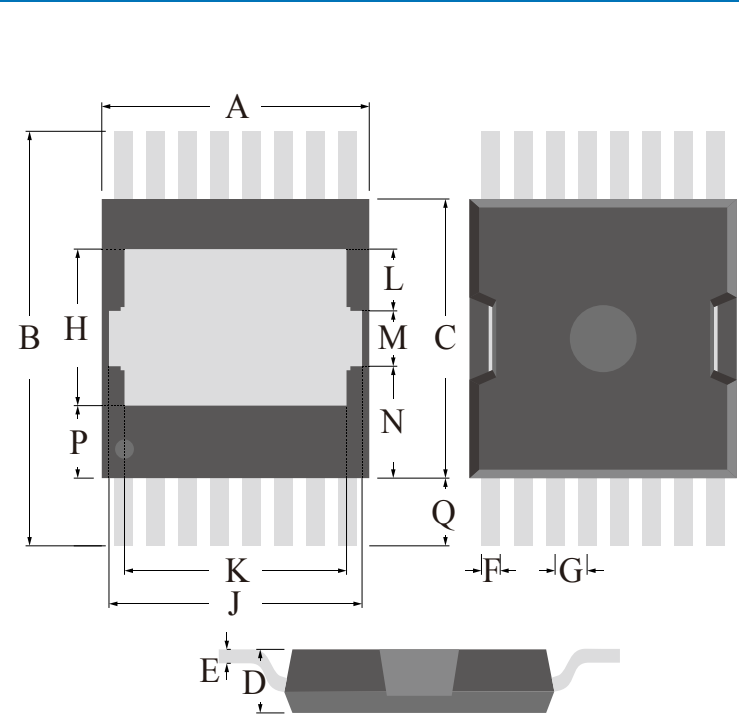
Fig.18- Diode Recovery Test Circuit & Waveform

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OUTLINE DRAWINGS



TOLT

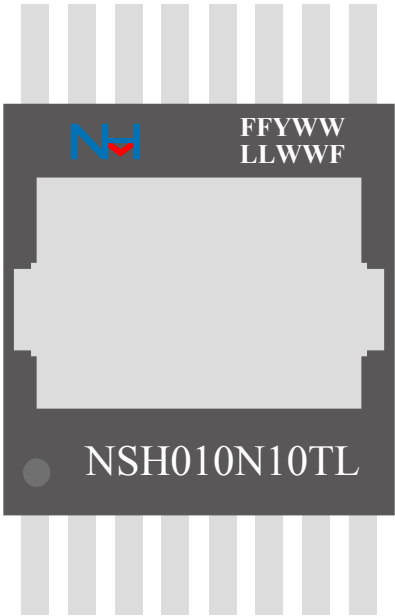
OUTLINE DIMENSIONS						
Dim.	Milimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	9.50	-	10.50	0.3740	-	0.4134
B	14.00	-	16.00	0.5512	-	0.6299
C	9.60	-	10.60	0.3780	-	0.4173
D	2.10	-	2.50	0.0827	-	0.0984
E	0.35	-	0.65	0.0138	-	0.0256
F	0.55	-	0.85	0.0217	-	0.0335
G	1.00	-	1.40	0.0394	-	0.0551
H	5.20	-	6.00	0.2047	-	0.2362
J	9.00	-	10.00	0.3543	-	0.3937
K	8.60	-	8.80	0.3386	-	0.3465
L	2.04	-	2.44	0.0803	-	0.0961
M	1.80	-	2.20	0.0709	-	0.0866
N	3.70	-	4.40	0.1457	-	0.1732
P	2.35	-	2.85	0.0925	-	0.1122
Q	2.25	-	2.65	0.0886	-	0.1043

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MARKING INFORMATION



MARKING INSTRUCTIONS

NH=Niuhang Trademark
FF=Product Line Code,According To Actual Changes
YWW=Date Code,According To Actual Changes
LLWWF=Inernal Code,According To Actual Changes
NSH010N10TL=Model

PACKING INFORMATION

Package Type	Package Code	Product Weight Approx(g/Pcs)	Package Method	Quantity (Pcs/Min. Pack.)	Quantity (Pcs/Inner Box)	Quantity (Pcs/Carton)
TOLT	P1	0	Tape & Reel	1800	1800	9000

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Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow

	Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
	Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
	Preheat-Temperature Min (T _S min) -Temperature Max (T _S max) -Time(t _s min to t _s max)	100°C 150°C 60-120 seconds	150°C 200°C 60-180 seconds
	Time maintained above: -Temperature (T _L) - Time (t _L)	183°C 60-150 seconds	217°C 60-150 seconds
	Peak Temperature(T _P)	240 +0/-5 °C	260 +0/-5 °C
	Time within 5°C of actual peak temperature (t _p)	10-30 seconds	20-40 seconds
	Ramp down rate	6°C/second max.	6°C/second max.
	Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

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Specification Revision History

Rev.	Date	Changed Items	Pre-Changed Content	Changed Content
A/1	2014-10-20	First Issue		
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